

3-PHASE CAPSTAN MOTOR DRIVER

The KA2822D is a monolithic integrated circuit, and suitable for the three-phase spindle motor driver of FDD system.

FEATURES

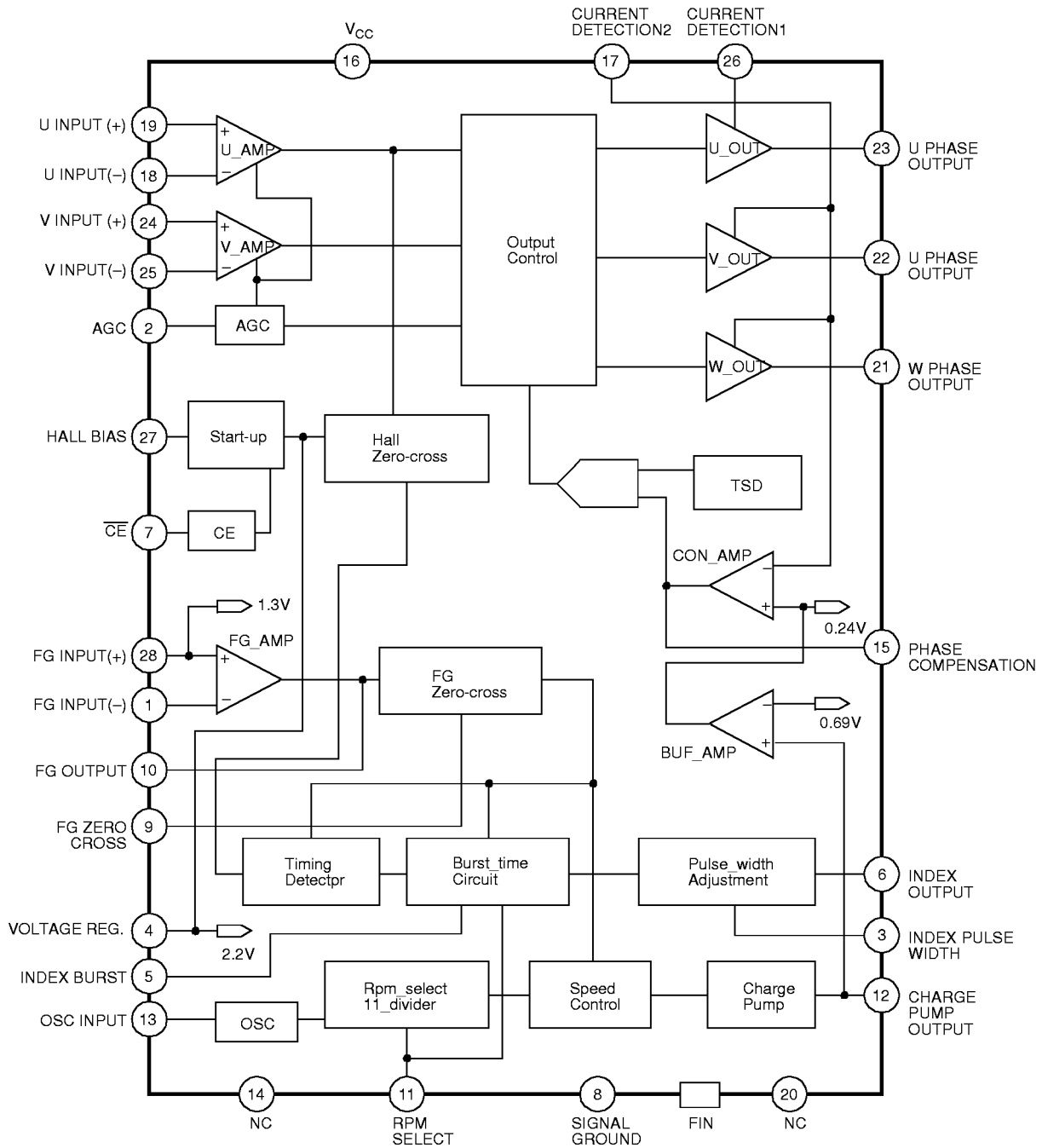
- 3-phase, full-wave, linear BLDC motor driver with 2 hall sensors
- Built-in soft switching drive circuit
- 300 or 360 RPM speed control
- Snubberless
- Built-in chip enable function
- Built-in digital speed control circuit
- Built-in current limit circuit
- Index sensorless
- Built-in TSD (Thermal shutdown)
- Low saturation voltage
- Digital input: TTL, 5V CMOS compatible
- Built-in current-mode control circuit (I_{PEAK} : 1A)



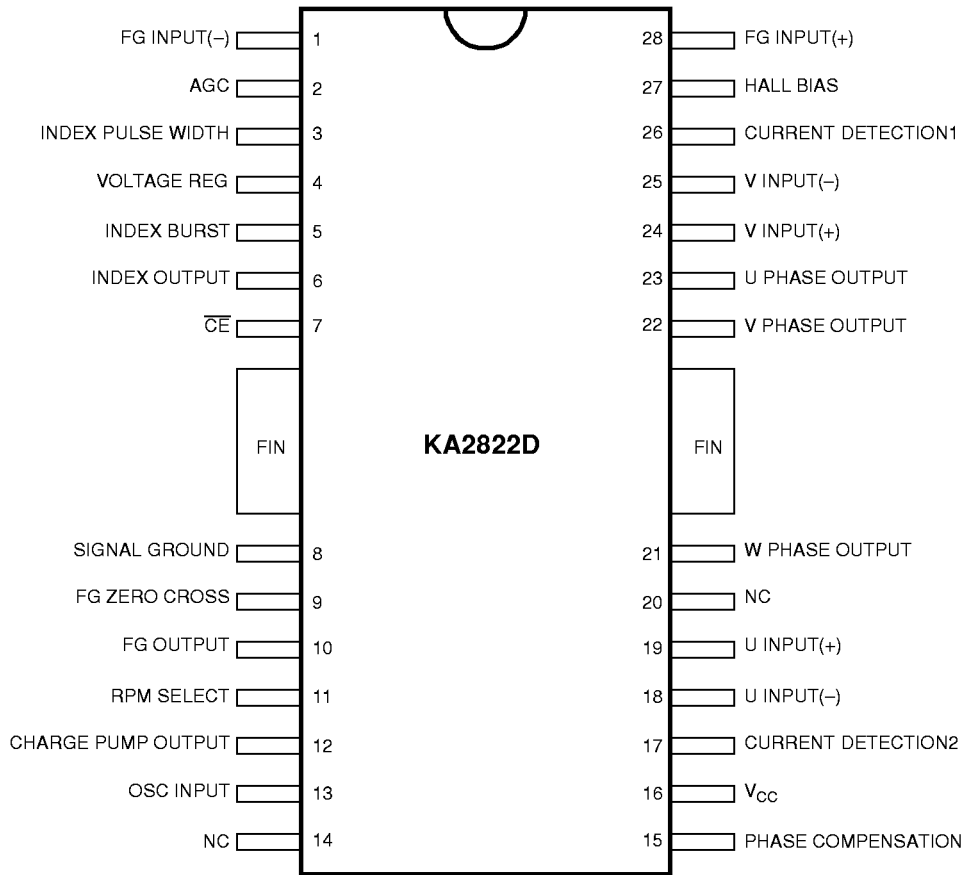
ORDERING INFORMATION

Device	Package	Operating Temperature
KA2822D	28-SSOPH-375	0 ~ 75°C

BLOCK DIAGRAM



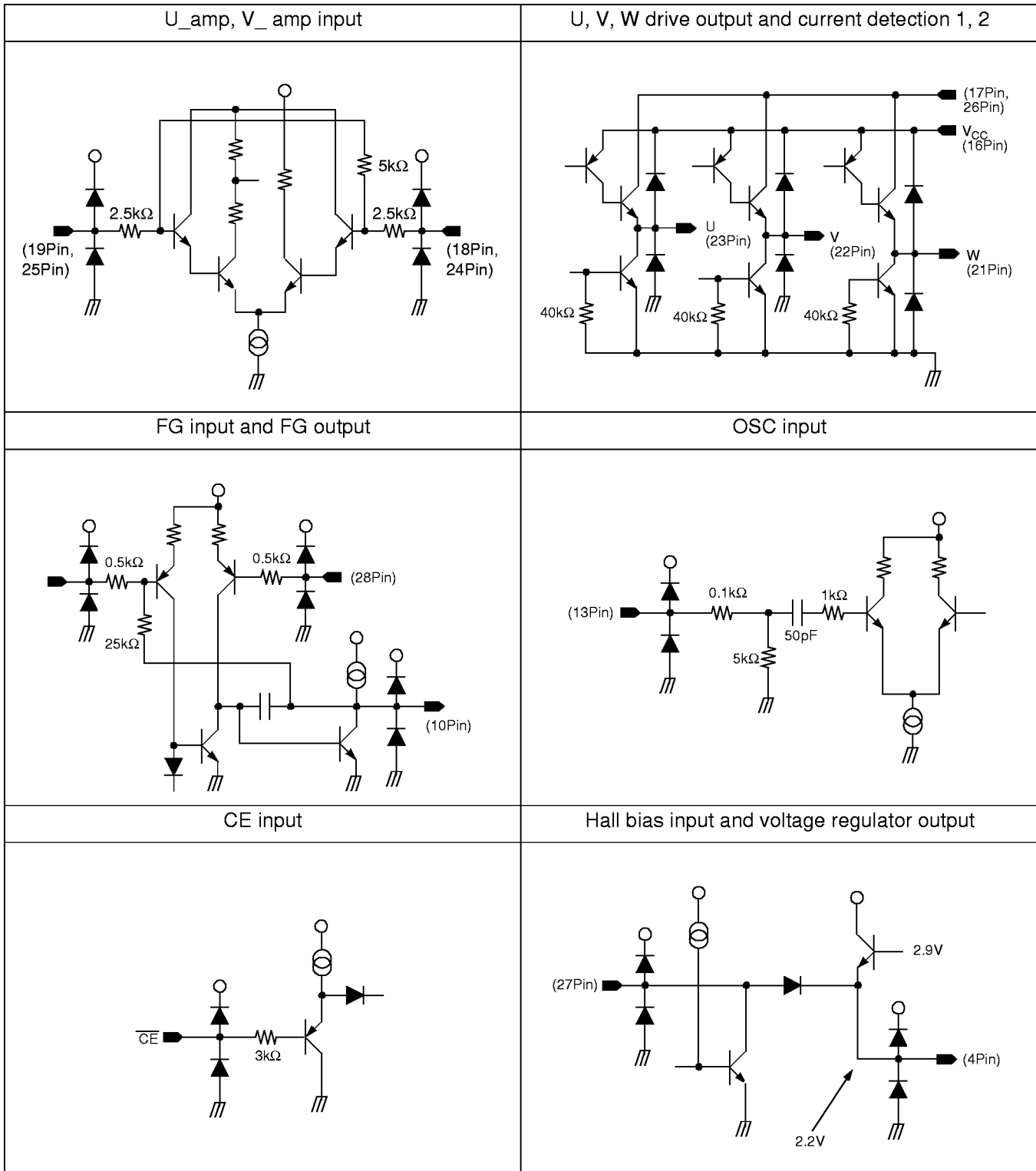
PIN CONFIGURATION



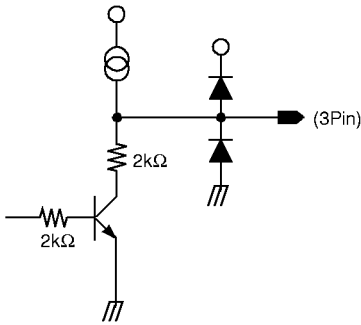
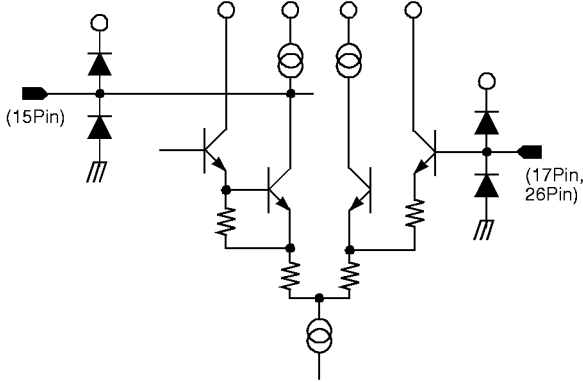
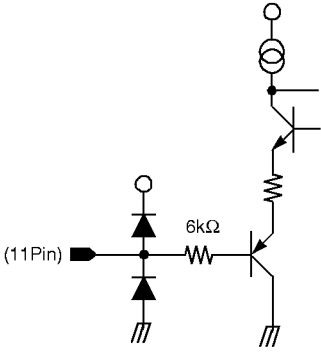
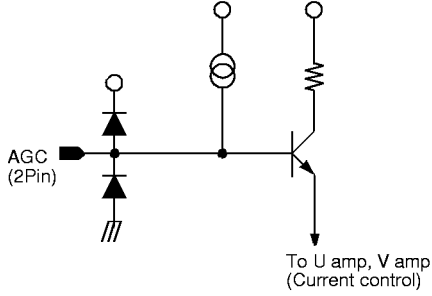
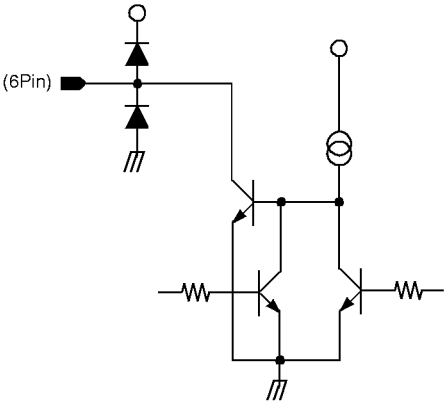
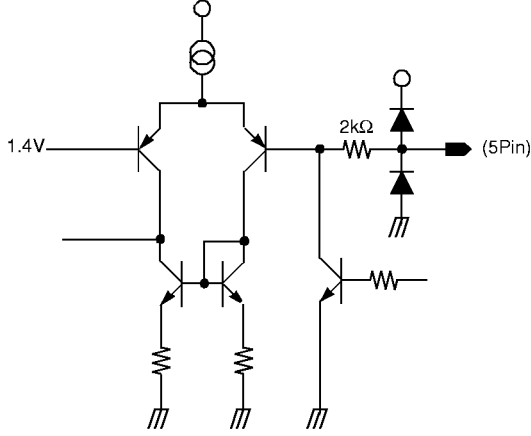
PIN DESCRIPTION

Pin No.	Symbol	I/O	Description
1	FG_INPUT	I	Negative input pin FG signal amp.
2	AGC	I	Automatic gain control input pin
3	INDEX PULSE WIDTH	O	Index pulse width detection (1.1V) pin
4	VOLTAGE REGULATOR	O	Voltage (2.2V) generator output pin
5	INDEX BURST	O	Index pulse width detection (1.4V) pin
6	INDEX OUTPUT	O	Index pulse output pin
7	CE	I	Chip enable (Active low)
8	SIGNAL GROUND	–	Signal ground
9	FG ZERO CROSS	O	FG signal zero cross detection pin
10	FG OUTPUT	O	FG signal output pin
11	RPM SELECT	I	RPM selection pin (L: 300, H: 360rpm)
12	CHARGE PUMP OUTPUT	O	Charge pump output pin
13	OSC INPUT	I	1MHz oscillation input pin
14	NC	–	No connection
15	PHASE COMPENSATION	I	Phase compensation cap. connection pin
16	V _{CC}	–	5V power supply pin
17	CURRENT DETECTION2	I	Over current detection pin
18	U INPUT (–)	I	Negative input pin of U phase amp
19	U INPUT (+)	I	Positive input pin of U phase amp
20	NC	–	No connection
21	W PHASE OUTPUT	O	W phase output pin
22	V PHASE OUTPUT	O	V phase output pin
23	U PHASE OUTPUT	O	U phase output pin
24	U INPUT (+)	I	Positive input pin of V phase amp
25	V INPUT (–)	I	Negative input pin of V phase amp
26	CURRENT DETECTION1	I	Over current detection pin
27	HALL BIAS	I	Hall sensor bias input pin
28	FG INPUT (+)	I	Positive input pin of FG signal amp
FIN	POWER GROUND	–	Power ground

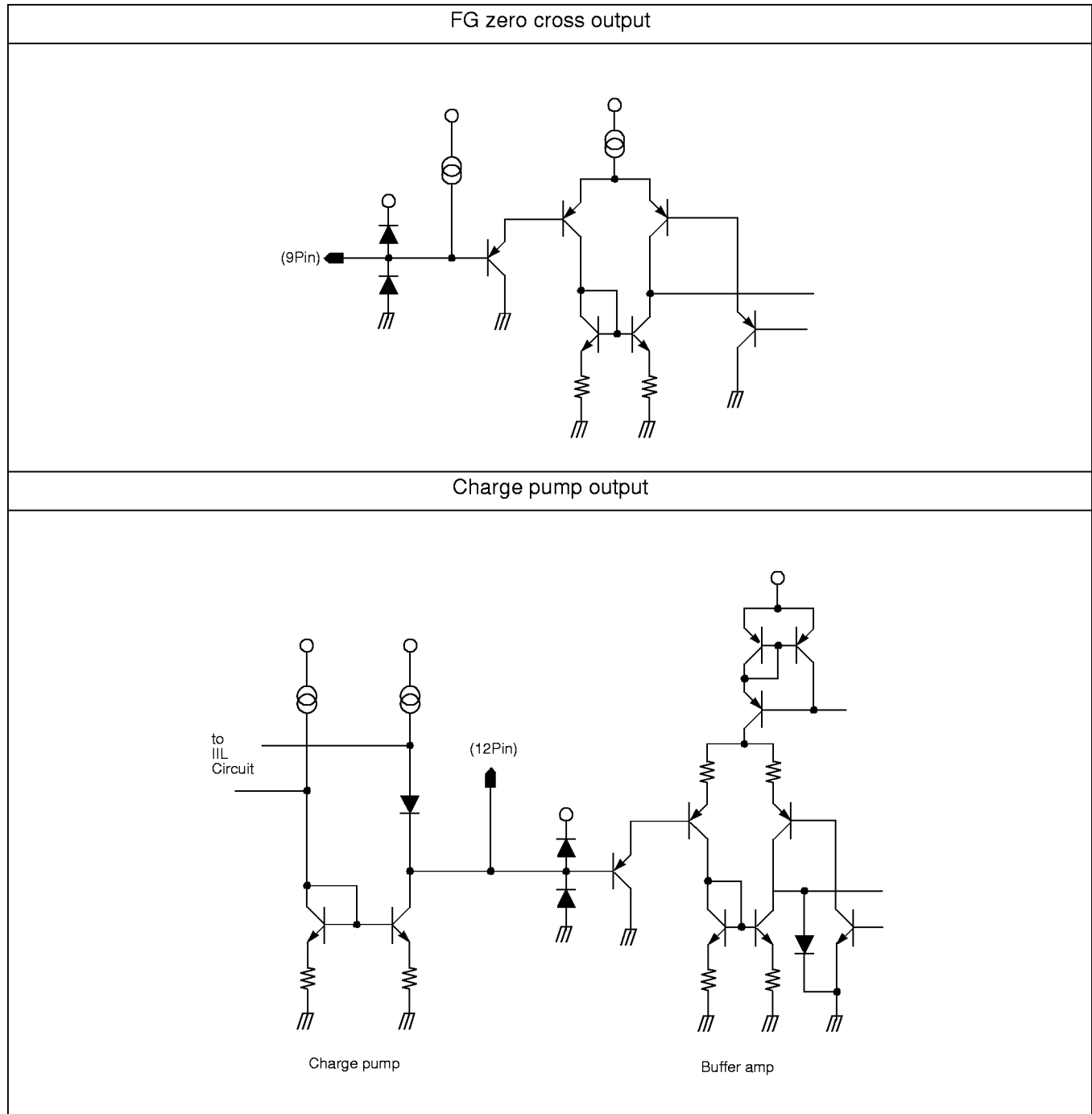
EQUIVALENT CIRCUITS



EQUIVALENT CIRCUITS (Continued)

<p>Index pulse width detection output</p> 	<p>Phase compensation and current detection 1, 2</p> 
<p>RPM select input</p> 	<p>AGC input</p> 
<p>Index output</p>	<p>Index burst output</p>
	

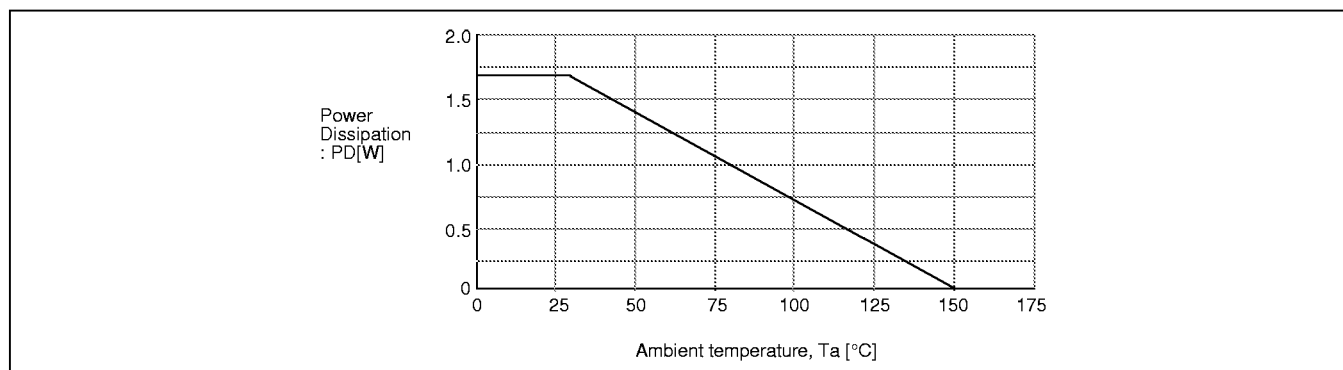
EQUIVALENT CIRCUITS (Continued)



ABSOLUTE MAXIMUM RATING

Characteristics	Symbol	Value	Unit
Maximum power supply voltage	V_{CCMAX}	7.0	V
Maximum input voltage	V_{INMAX}	0 ~ V_{CC}	V
Maximum output current	I_{OMAX}	1	A
Normal output current	I_O	0.7	A
Power dissipation	P_D	1.5	W
Operating temperature	T_A	0 ~ 75	°C
Junction temperature	T_J	150	°C
Storage temperature	T_{STG}	-55 ~ +125	°C

POWER DISSIPATION CURVE



Power dissipation decreases in the rate of 13.5mW / °C when mounted on 50mm × 50mm × 1mm PCB (Phenolic resin material) and used above $T_a=25^\circ\text{C}$.

RECOMMENDED OPERATING CONDITIONS

Characteristics	Symbol	Min.	Typ.	Max.	Unit
Supply voltage	V_{CC1}, V_{CC2}	10.8	12.0	13.2	V
Supply voltage in logic part	V_{CC3}	4.5	5.0	5.5	V
Ambient operating temperature range	T_a	0	–	+70	°C

TEMPERATURE CHARACTERISTIC

Characteristics	Symbol	Min.	Typ.	Max.	Unit
Thermal shutdown temperature ^(note)	TSD	125	150	–	°C

NOTE: Reference value

ELECTRICAL CHARACTERISTICS

(Ta=25°C, V_{CC}=5V)

Characteristic	Symbol	Test conditions	Min.	Typ.	Max.	Unit
SUPPLY CURRENT						
Supply current 1	I _{CCO}	V _{CC} =6.5V CE=H, RPM=L	–	1.0	2.0	mA
Supply current 2	I _{CC}	V _{CC} =6.5V, CE=L	–	15	23	mA
CHIP ENABLE						
Input current	I _{CE}	CE=0~5V	–	5	10	μA
Input low voltage	V _{CE1}	–	–	–	1.0	V
Input high voltage	V _{CEH1}	–	3.5	–	–	V
RPM SELECT						
Input current	I _{RPM}	CE=0~5V	–	5	10	μA
Input low voltage	V _{RPM1}	–	–	–	1.0	V
Input high voltage	V _{RPMH}	–	3.5	–	–	V
HALL AMP						
Input resistance ^{note}	R _{IN}	–	1	10	–	–
Common mode input voltage range ^{note}	V _{COM}	–	2.0	–	V _{CC}	V
Differential input voltage range ^{note}	V _{DIF}	–	70	–	210	mV _{P-P}
START-UP						
Hall bias voltage 1	V _{HB1}	I _H =4mA, CE=L	2.3	2.5	2.7	V
Hall bias voltage 2	V _{HB2}	I _H =10mA, CE=L	2.7	2.9	3.1	V
Reference voltage	V _{ref}	I _O =1mA, CE=L	1.7	2.2	2.7	V
Bias off current	I _{HOFF}	V _H =7V, CE=H	–	5	10	μA
OUTPUT AMP						
Leakage current	I _{CER}	–	–	0.5	1	mA
Saturation voltage 1	V _{sat1}	I _O =0.35A	–	1.0	1.2	V
Saturation voltage 2	V _{sat2}	I _O =0.7A	–	1.3	1.8	V
BUFFER & CONTROL AMP						
Voltage gain 1 ^{note}	G _{CT1}	–	–	–11	–	dB
Reference voltage 1 ^{note}	V _{ref1}	Current limiter voltage	0.215	0.24	0.265	V
Reference voltage 2 ^{note}	V _{ref2}	Control begin voltage	–	0.69	–	V

NOTE: Reference value

ELECTRICAL CHARACTERISTICS (Continued)(Ta=25°C, V_{CC}=5V)

Characteristic	Symbol	Test conditions	Min.	Typ.	Max.	Unit
CHARGE PUMP						
Charge current	I _{CP-}	RPM=L	-15	-20	-25	μA
Discharge current	I _{CP+}	RPM=L	15	20	25	μA
Current ratio ^{note}	I _R	I _{CP+} / I _{CP-}	0.9	1.0	1.1	-
Off current	I _{OFF}	V _{CP} =0.63V	-	-	50	nA
Clamp voltage ^{note}	V _{CPLP}	-	-	1.3	1.5	V
FG AMP						
Output DC voltage ^{note}	V _{FG}	-	1.0	1.3	1.6	V
Voltage gain 2	G _{FG}	-	24	34	44	dB
Input voltage range ^{note}	V _{IN}	-	2.0	-	20	mV _{P-P}
Noise margin 1 ^{note}	N _D	Differential noise	-	-	0.5	mV _{P-P}
Noise margin 2 ^{note}	N _C	Common mode noise	-	-	0.5	V _{P-P}
SPEED CONTROL						
Count range 1 ^{note}	N1	RPM=L	-	1666.5	-	-
Count range 2 ^{note}	N2	RPM=H	-	1388.5	-	-
Operating freq.	F _D	-	-	1.0	1.1	MHz
BURST ADJUSTMENT						
Input current	I _{BI}	-	-	1	2	μA
Threshold voltage 1	V _{TH1}	RPM=L	1.2	1.45	1.7	V
Threshold voltage 2	V _{TH2}	RPM=H	1.05	1.3	1.55	V
PULSE WIDTH ADJUSTMENT						
Ct2 charge current	I _{CT2}	-	-19	-25	-36	μA
Threshold voltage 3	V _{TH3}	-	0.9	1.1	1.3	V
INDEX OUTPUT						
Output leakage current ^{note}	I _{OH}	-	-	1	2	μA
Output low voltage	V _{O1}	I _O =1mA	-	0.2	0.4	V

NOTE: Reference value

APPLICATION INFORMATION

1. CHIP ENABLE

This function turns on or off all blocks by low or high signal.

2. U, V AND W PHASE OUTPUT AMP

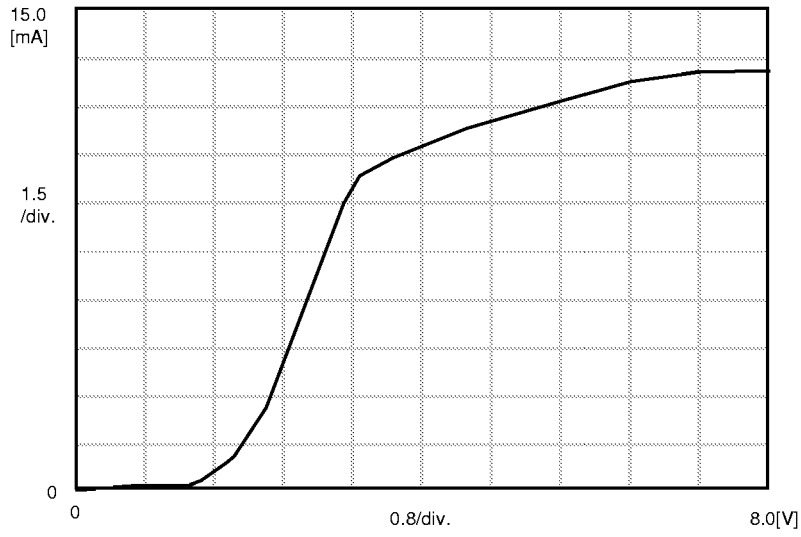
This part drives the output as making U, V and W current waveform having 120° phase difference with using the current ratio of each amp output after giving the signal occurred by 2 hall sensors to U amp and V amp.

3. SPEED CONTROL PART

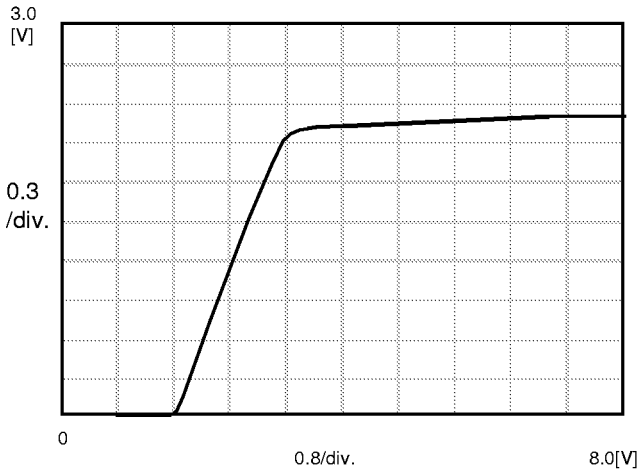
This function compares the real motor rotation frequency with the 300 or 360Hz pulse divided from 1MHz clock pulse for removing speed error when motor is on and speed error is detected by PLL.

The speed error sent to charge pump part which repeats charge and discharge controls the output current of the output amp to keep a stable rotation.

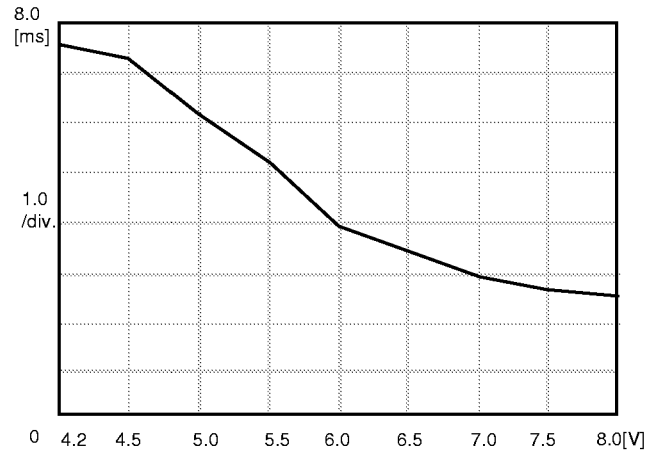
GRAPHS



1. V_{CC} vs. I_{CC}

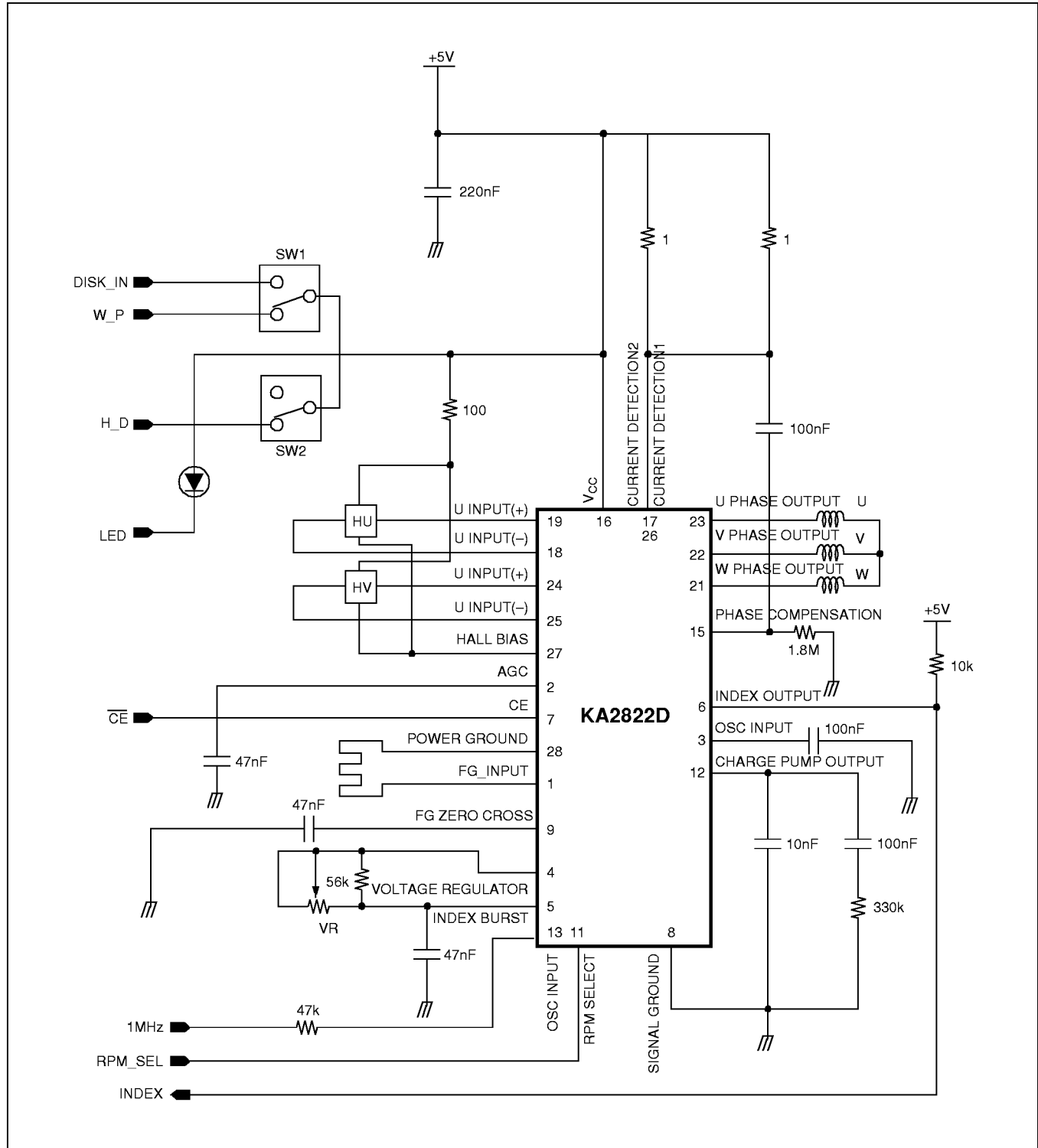


V_{CC} vs. V_{ref} (Pin4)



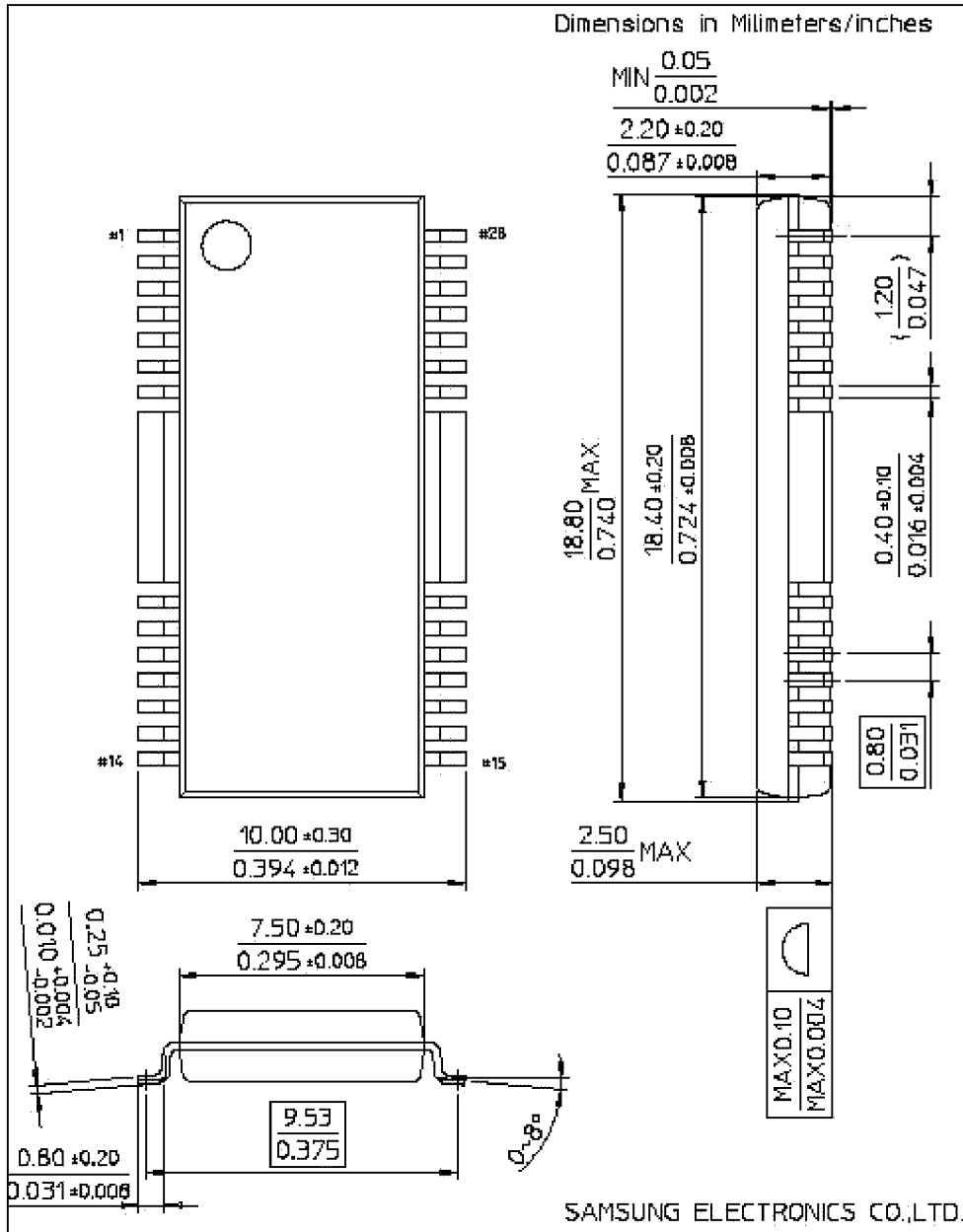
V_{CC} vs. Index pulse width

APPLICATION CIRCUIT



PACKAGE DIMENSION

28-SSOPH-375



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