

**DL11
asynchronous
line interface
engineering drawings**

digital equipment corporation · maynard, massachusetts

CUSTOMER PRINT SET INDEX

SEQUENCE ⌞ ⌞ ⌞ SEQUENCE ⌞ ⌞ ⌞

PRINT SET #1

DRAWING DIRECTORY
ASYNCRONOUS LINE INTERFACE
ASYNCRONOUS LINE INTERFACE (PL)
ASYNCRONOUS LINE INTERFACE
CABLE ASSEMBLY (KL8/E)
SOFTWARE LIST
ACCESSORY LIST
INSTALLATION PROCEDURE

B-DD-DL11-0
C-UA-DL11-0-0
A-PL-DL11-0-0
E-CS-M7800-YA-1
D-IA-7008360-0-0
A-SL-DL11-0-4
A-AL-DL11-0-5
A-SP-DL11-0-2

PRINT SET #3

DRAWING DIRECTORY
ASYNCRONOUS LINE INTERFACE
ASYNCRONOUS LINE INTERFACE (PL)
ASYNCRONOUS LINE INTERFACE
CABLE, MODEM BC05C
CABLE ASSEMBLY (KL81E)
MODEM TEST CONN.
INSTALLATION PROCEDURE

B-DD-DL11-0
C-UA-DL11-0-0
A-PL-DL11-0-0
E-CS-M7800-0-1
D-UA-BC05C-0-0
D-IA-7008360-0-0
D-CS-H315-0-1
A-SP-DL11-0-2

PRINT SET #2

DRAWING DIRECTORY
ASYNCRONOUS LINE INTERFACE
ASYNCRONOUS LINE INTERFACE (PL)
ASYNCRONOUS LINE INTERFACE
CABLE, MODEM BC05C
FILTER NETWORK
MODEM TEST CONN
SOFTWARE LIST
ACCESSORY LIST
INSTALLATION PROCEDURE

B-DD-DL11-0
C-UA-DL11-0-0
A-PL-DL11-0-0
E-CS-M7800-0-1
D-UA-BC05C-0-0
B-CS-G8000-0-1
D-CS-H315-0-1
A-SL-DL11-0-4
A-AL-DL11-0-5
A-SP-DL11-0-2

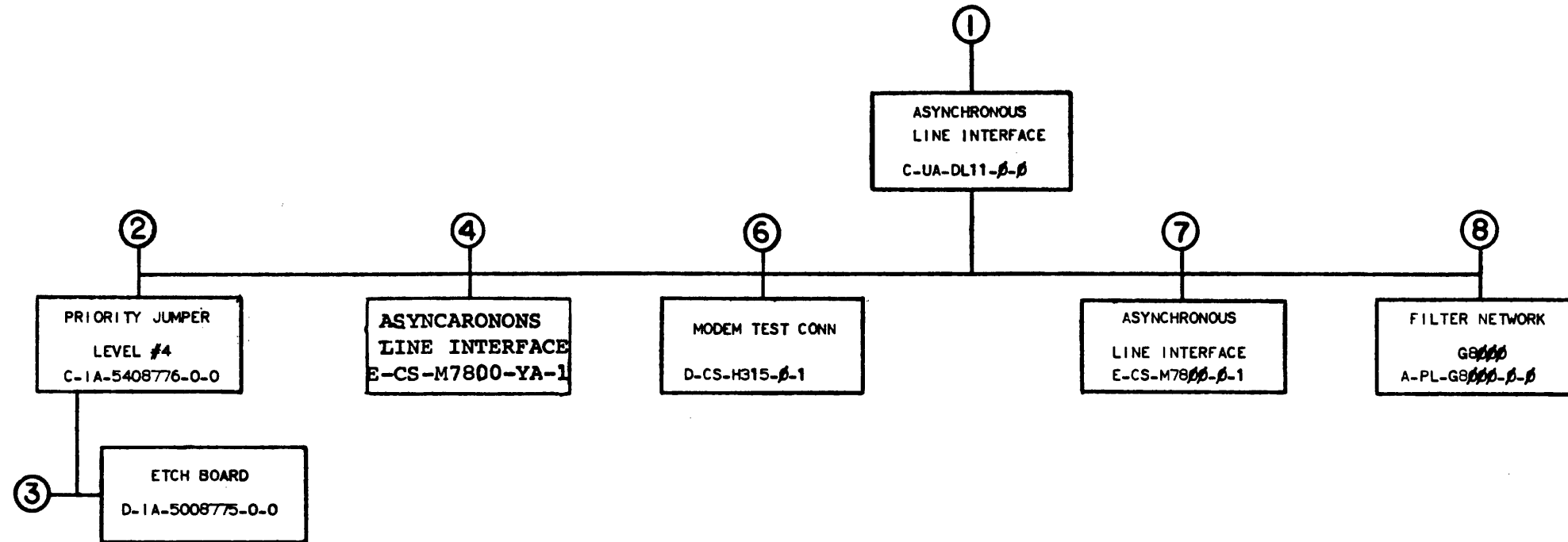
THIS IS PRINT SET

UNIT VARIATIONS		PRINT SET	
VAR	TITLE	DL11-1	DL11-2
DL11-A	ASync LINE INTERFACE, CURRENT LOOP	1	0
DL11-B	ASync LINE INTERFACE, EIA	0	1
DL11-C	ASync LINE INTERFACE, CURRENT LOOP	1	0
DL11-D	ASync LINE INTERFACE, EIA	0	1
DL11 E	ASync LINE INTERFACE, DATA SET	0	1

REVISIONS

DATE	CHG. NO.	REV
2-76	DL11-00009	K
3/79	DL11-10	L

USED ON OPTION/MODEL		DRN. M. Pierce	DATE 4-28-72	TITLE ASYNCHRONOUS LINE INTERFACE	
		CHK'D. R. Cook	DATE 5-9-72		
		PROJ ENG. P.E.Janson	DATE 5-11-72		
		PROD. J.McIntyre	DATE 5-15-72	SIZE B	CODE DD
		FIELD SERV. R. Evans	DATE 5-15-72	NUMBER DL11-0	REV L
SHEET 1 OF 3		DIST			

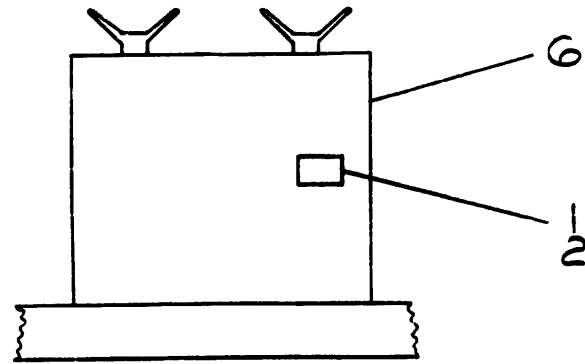


TITLE	ASYNCHRONOUS LINE INTERFACE	SHEET 2 OF 3	SIZE CODE B DD	NUMBER DL11-Ø	REV L
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CUSTOMER PRINT SET				ELECTRICAL						CUSTOMER PRINT SET				MECHANICAL						
DL11-1	DL11-2	DL11-3	DEPT SET	FIND NO.	DRAWING NO.	REV	NO OF SHT	DESCRIPTION	OPTION NO.	DL11-1	DL11-2	DL11-3	DEPT SET	FIND NO.	DRAWING NO.	REV	NO OF SHT	DESCRIPTION	OPTION NO.	
X	X	X		1.	C-UA-DL11-β-β	F	1	ASYNCHRONOUS LINE INTERFACE						1.	C-UA-DL11-β-β	F	1	ASYNCHRONOUS LINE INTERFACE		
X	X	X			A-PL-DL11-β-β	H	1	ASYNCHRONOUS LINE INTERFACE (PL)							A-PL-DL11-β-β	F	1	ASYNCHRONOUS LINE INTERFACE (PL)		
	X	X			D-UA-BCβ5C-β-β	#	1	CABEE. MODEM. BCβ5C							D-UA-BCβ5C-β-β		1	CABLE, MODEM BCβ5C		
X		X			D-1A-7008360-0-0	#	1	CABLE, ASSEMBLY (KL8/E)							D-1A-7008360-0-0		1	CABLE ASSEMBLY (KL8/E)		
					A-SP-DL11-β-1	*	11	ENGINEERING SPECIFICATION												
X	X	X			A-SP-DL11-β-2	H	11	INSTALLATION PROCEDURE												
					A-SP-DL11-β-3	B	8	TEST PRODEDURE (TEST & ACCEPTANCE)												
X	X				A-SL-DL11-β-4	*	1	SOFTWARE LIST												
X	X				A-AL-DL11-β-5	D	1	ACCESSORY LIST												
				2.	C-1A-5408776-0-0		1	PRIORITY JUMPER LEVEL #4						2.	C-1A-5408776-0-0		1	PRIORITY JUMPER LEVEL #4		
					B-CS-5408776-0-1		1	CIRCUIT SCHEMATIC							K-CO-5408776-0-4		1	X-Y COORDINATE HOLE LOC		
					K-CO-5408776-0-4		1	X-Y COORDINATE HOLE LOC							B-MH-5408776-0-6		1	ASSY/DRILLING HOLE LAYOUT		
					B-MH-5408776-0-6		1	MODULE ECO HISTORY												
				3.	C-AH-5408776-0-5		1	ASSY/DRILLING HOLE LAYOUT						3.	D-1A-5008775-0-0		1	ETCH BOARD		
															C-AH-5408776-0-5		1	ASSY/DRILLING HOLE LAYOUT		
X				4	E-CS-M7800-YA-1	#	6	ASYNCHRONOUS LINE INTERFACE												
					K-CO-M7800-YA-4		1	X-Y COORDINATE HOLE LOCATION												
					D-AH-M7800-YA-5		1	ASSY DRILLING HOLE LAYOUT												
					B-MH-M7800-YA-6		1	MODULE ECO HISTORY												
				6.	D-CS-H315-β-1	#	1	MODEM TEST CONN						6.	D-CS-H315-β-1		1	MODEM TEST CONN		
	X	X			K-CO-H315-β-4		1	X-Y COORDINATE HOLE LOC							K-CO-H315-β-4		1	X-Y COORDINATE HOLE LOC		
					D-AH-H315-β-5		1	ASSY DRILLING HOLE LAYOUT							C-AH-H315-β-5		1	ASSY/DRILLING HOLE LAYOUT		
					B-MH-H315-β-6		1	MODULE ECO HISTORY							B-MH-H315-β-6		1	MODULE ECO HISTORY		
X	X	X		7.	E-CS-M7800-β-1	#	7	ASYNCHRONOUS LINE INTERFACE						7.	E-CS-M7800-β-1		7	ASYNCHRONOUS LINE INTERFACE		
					K-CO-M7800-β-4		1	X-Y COORDINATE HOLE LOC							K-CO-M7800-β-4		1	X-Y COORDINATE HOLE LOC		
					D-AH-M7800-β-5		1	ASSY/DRILLING HOLE LAYOUT							D-AH-M7800-β-5		1	ASSY/DRILLING HOLE LAYOUT		
					B-MH-M7800-β-6		1	MODULE ECO HISTORY							B-MH-M7800-β-6		1	MODULE ECO HISTORY		
				8.	A-PL-G8000-β-β		1	FILTER NETWORK						8.	A-PL-G8000-β-β		1	FILTER NETWORK		
X					B-CS-G8000-β-1	#	1	CIRCUIT SCHEMATIC							K-CO-G8000-β-4		1	X-Y COORDINATE HOLE LOC		
					K-CO-G8000-β-4		1	X-Y COORDINATE HOLE LOC							C-AH-G8000-β-5		1	ASSY/DRILLING HOLE LAYOUT		
					C-AH-G8000-β-5		1	ASSY/DRILLING HOLE LAYOUT							B-MH-G8000-β-6		1	MODULE ECO HISTORY		
					B-MH-G8000-β-6		1	MODULE ECO HISTORY												
										TITLE ASYNCHRONOUS LINE INTERFACE				SHEET 3 OF 3		SIZE B DD		CODE NUMBER DL11-β		REV L

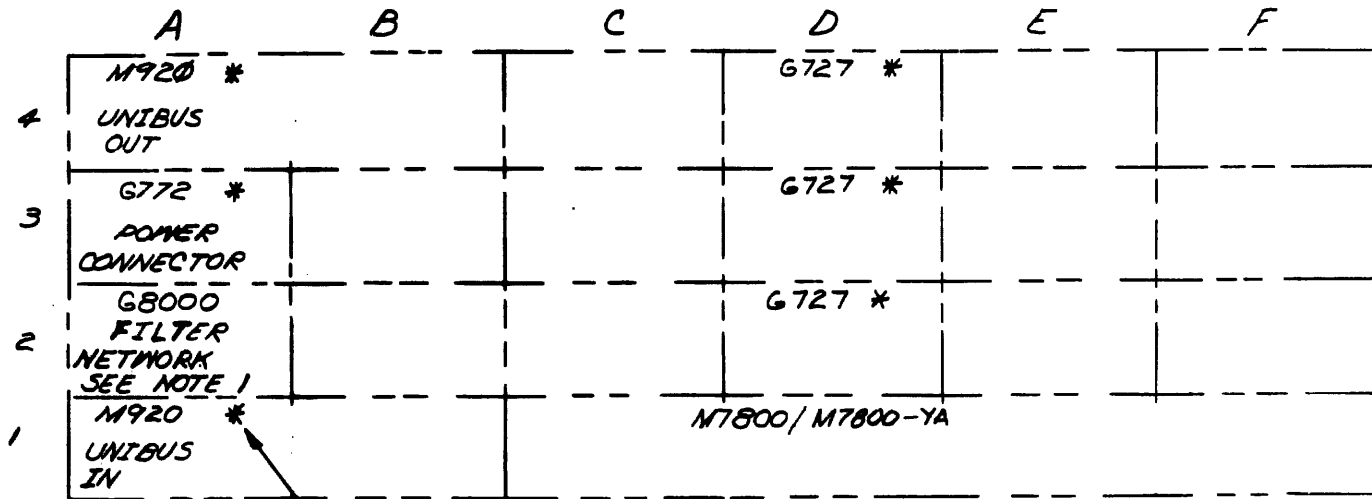
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NOTES:

- G 8000 IS REQUIRED ONLY IN PDP 11 SYSTEMS WHERE +15V IS NOT AVAILABLE. THE INSTALLATION REQUIRES 2 WIRES TO BE ADDED.
A03V2-A02V2
A02N2-CXXUI
WHERE (XX) IS THE SLOT NUMBER CONTAINING THE DLII.
- ITEMS INDICATED WITH ASTERISK (*) ARE SHOWN FOR REFERENCE ONLY AND ARE NOT PART OF THIS UNIT.



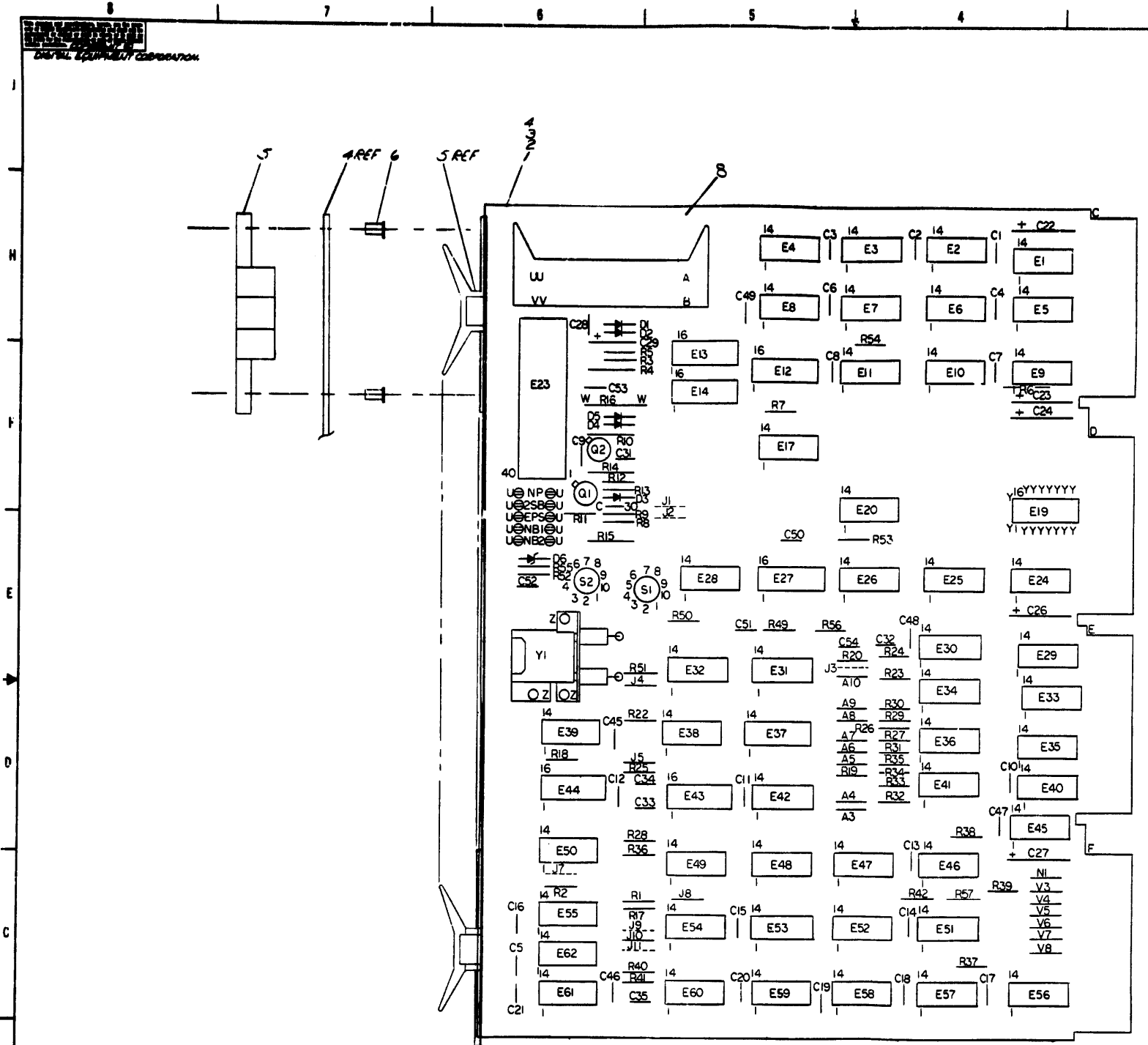
SEE NOTE 2

REV.	CHG. NO.	REV.
A	DLII-0000	
B	DLII-0001	
C	DLII-0005	
D	DLII-0006	
E	DLII-0008	
F	DLII-0009	
G	DLII-0010	

FIRST USED ON OPTION/MODEL	QTY.	DESCRIPTION	PART NO.	ITEM NO.
PDP-11		PARTS LIST		
UNLESS OTHERWISE SPECIFIED DIMENSION IN INCHES. TOLERANCES	DRN. <i>M. Riva</i>	DATE <i>7/18/72</i>	digital EQUIPMENT CORPORATION MAYNARD, MASSACHUSETTS	
DECIMALS .XXX = .005 .XX = .02 .X = .1	CHK'D. <i>J. F. Jensen</i>	DATE <i>4-29-72</i>	TITLE	
ANGLES ±0° 30'	ENG. <i>R. E. Janson</i>	DATE <i>5-11-72</i>	ASYNCHRONOUS LINE INTERFACE	
REMOVE BURRS AND BREAK SHARP CORNERS SURFACE QUALITY ✓	PROL ENG. <i>R. E. Janson</i>	DATE <i>5-11-72</i>		
MATERIAL	BROD. <i>J. M. Deane</i>	DATE <i>5-15-72</i>		
FINISH	NEXT HIGHER ASSY.		SIZE CODE	NUMBER
			CUA	DLII-0-0
			DIST. G	REV. H

REV. H
NUMBER DLII-0-0
SIZE CODE CUA

DIGITAL EQUIPMENT CORPORATION MAYNARD, MASSACHUSETTS PARTS LIST					QUANTITY / VARIATION															
MADE BY M. PIERCE		CHECKED J. FERGUSON		SECTION	DL11-A	DL11-B	DL11-C	DL11-D	DL11-E											
DATE 4/27/72		DATE 4/27/72		1																
ENG P. E. JANSON		PROD <i>J. Mc Ghee</i>		ISSUED SECT.																
DATE 5/11/72		DATE 5/15/72		1																
ITEM NO.	DWG NO. / PART NO.	DESCRIPTION			DL11-A	DL11-B	DL11-C	DL11-D	DL11-E											
1	C-IA-5408776-0-0	PRIORITY JUMPER LEVEL #4			1	1	1	1	1											
3	D-UA-BC05C-25	CABLE MODEM BC05C			-	1	-	1	1											
4	D-IA-7008360-0-0	CABLE ASSEMBLY (KL8E)			1	-	1	-	-											
5	D-CS-M315-0-1	MODEM TEST CONNECTOR																		
6	E-CS-M7800-0-1	ASYNCHRONOUS LINE INTERFACE			-	1	-	1	1											
7	A-PL-G8000-0-0	FILTER NETWORK																		
8		CRYSTAL			A/R	A/R	A/R	A/R	A/R											
9	E-CS-M7800-YA-1	ASYNCHRONOUS LINE INTERFACE			1	-	1	-	-											
10	9008269	TRANSPARENT VINYL TAPE			A/R															
NOTES:																				
1. G8000 IS REQUIRED ONLY IN PDP11 SYSTEMS WHERE +15V IS NOT AVAILABLE. ONE PER DD11-A																				
2. ONE M315 PER PDP11 SYSTEM																				
3. CRYSTAL FREQUENCY DEFINED BY CUSTOMER SPECIFIED BAUD RATE OR BY THE DOCUMENTATION OF AN OPTION WHICH USES THE DL11.																				
4. APPLY TAPE TO TOP SURFACES OF CRYSTAL AND MOUNTING BRACKETS TO INSULATE FROM ADJACENT MODULES.																				
5. PRIORIY LEVELS 5, 6, or 7 MAY BE SPECIFIED BY THE GUSTOMER OR THE DOCUMENTATION OF AN OPTION WHICH USES THE DL11.																				
TITLE					ASSY NO.					SIZE		CODE		NUMBER			REV.		ECO NO.	
ASYNCHRONOUS LINE INTERFACE					C-UA-DL11-0-0					A		PL		DL11-0-0			A		DL11-00010	
					SHEET 1 OF 1					DIST.										

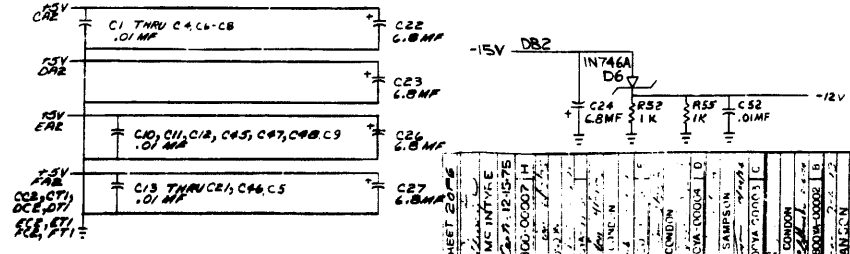


NOTES:
 1.) PIN NOTATION THROUGHOUT IS ORDERED UPON MODULE PLACEMENT IN THE SYSTEM UNIT. MODULE REFERENCE ALONE IS OBTAINED BY CONVERTING THE FIRST LETTER ACCORDING TO THE PIN NOMENCLATURE CHART AT THE LEFT.
 2.) NUMBERS TO BE USED AT CONNECTIONS A3-A10, J4-J5, J8, J10, V3-V8, AND N1.
 3.) LETTERS ENCLOSED IN PARENTHESIS REFER TO PINS ON THE BERG CONNECTOR. EXAMPLE: (X).
 4.) DEC 8640C WERE PHASED IN AS 380 REPLACEMENTS ANY 380 FAILURES SHOULD BE REPLACED BY 8640C, EXCEPT E28. E28 MUST BE REPLACED WITH A 7380 CHIP.

PIN NOMENCLATURE
 MODULE SYSTEM UNIT

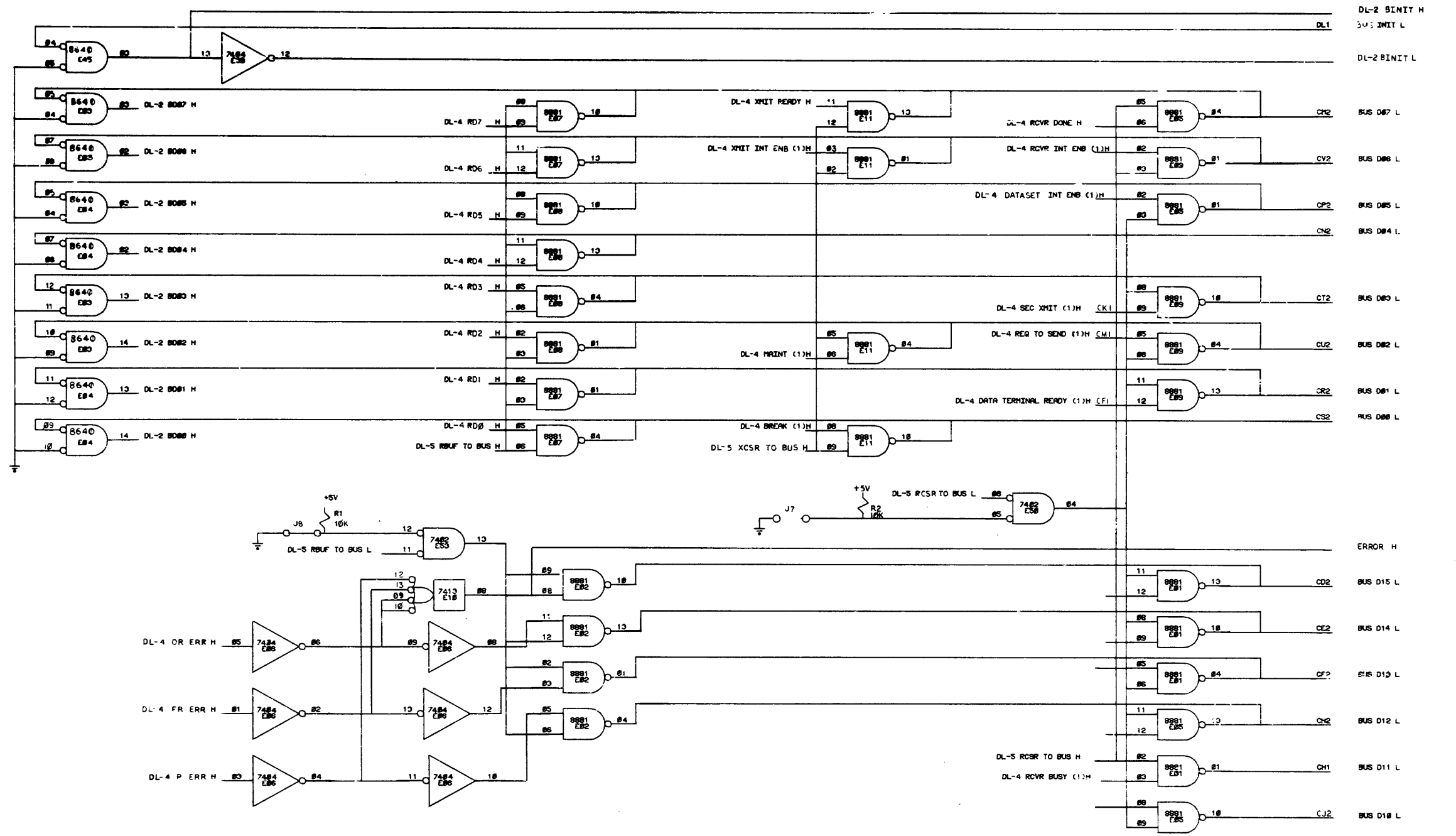
QTY	REF DESIGNATION	DESCRIPTION	PART NO.	NO.
3		WASHERS, INTERNAL TOOTH LOCK	30046631	70
1	E28	IC DEC 7380	1910390	69
19	J1 THRU J19	JUMPER, INSULATED	900185	45
1	R3	RES 750R 1/4W 5%	1301401	47
7	R38	RES 330 A 1/4W 5%	1300309	46
1	D6	DIODE 1N746A	1104860	65
1	Q2	TRANSISTOR 2N3638	150340100	64
1	C53	CAP 100P 100V 5% MICA	10000016	63
1	C54	CAP 500P 100V 5% MICA	10000025	62
1	C50, C51	CAP .047MFD CERAMIC	1000678	61
1	E27	IC DEC 74161	1910650	60
2	C54, C55	OP 330P 100V 5% DIPPED MICA	10000023	59
1	C52	OP 200P 100V 5% DIPPED MICA	10000020	58
1	C51	OP 100P 100V 5% DIPPED MICA	10000012	57
29	C1, C2, C3, C4, C5, C6, C7, C8, C9, C10, C11, C12, C13, C14, C15, C16, C17, C18, C19, C20, C21, C22, C23, C24, C25, C26, C27, C28, C29, C30, C31, C32, C33, C34, C35, C36, C37, C38, C39, C40, C41, C42, C43, C44, C45, C46, C47, C48, C49, C50, C51, C52, C53, C54, C55, C56, C57, C58, C59	OP 100P 100V 5% DIPPED MICA	10000012	56
1	C29	CAP .47UF 35V 10% TANT	1003963	55
5	R2, R3, R4, R5, R6, R7, R8, R9, R10, R11, R12, R13, R14, R15, R16, R17, R18, R19, R20, R21, R22, R23, R24, R25, R26, R27, R28, R29, R30, R31, R32, R33, R34, R35, R36, R37, R38, R39, R40, R41, R42, R43, R44, R45, R46, R47, R48, R49, R50, R51, R52, R53, R54, R55, R56	RES 1K 1/4W 5%	1300325	54
5	R14, R15, R23, R24, R40, R41, R42, R43, R44, R45, R46, R47, R48, R49, R50, R51, R52, R53, R54, R55, R56	RES 1.5K 1/4W 5%	1300394	53
1	R47	RES 47K 1/4W 5%	1300202	52
1	R10	RES 82K 1/4W 5%	1301057	51
1	R9	RES 82K 1/4W 5%	1301057	50
1	R8	RES 100K 1/4W 5%	1300259	49
1	R11	RES 100K 1/4W 5%	1300259	48
1	R42, R43	RES 100K 1/4W 5%	1300259	47
1	R44, R45	RES 100K 1/4W 5%	1300259	46
1	R46, R47, R48, R49, R50, R51, R52, R53, R54, R55, R56	RES 200K 1/4W 5%	1300274	45
5	R12, R13, R51	RES 200K 1/4W 5%	1300316	44
1	R4	RES 560K 1/4W 5%	1300398	43
3	R16	NUT 1/8" X 7/16"	9004555	40
1	R14, R2, R6, R7, R11, R13, R17, R18, R19, R21, R22, R25, R26, R27, R28, R29, R31, R32, R33, R34, R35, R36, R37, R38, R39, R40, R41, R42, R43, R44, R45, R46, R47, R48, R49, R50, R51, R52, R53, R54, R55, R56	RES 10K 1/4W 5%	1300385	39
24	R50, R51, R52, R53, R54, R55, R56	RES 1.5K 1/4W 5%	1300394	37
1	R42	RES 100K 1/4W 5%	1300316	36
1	R43	RES 100K 1/4W 5%	1300316	35
4	E28, E29, E30, E31, E32, E33, E34, E35, E36, E37, E38, E39, E40, E41, E42, E43, E44, E45, E46, E47, E48, E49, E50, E51, E52, E53, E54, E55, E56, E57, E58, E59, E60, E61	IC DEC 7474	2114137-50	34
2	E36, E41	IC DEC 8242	1909712	33
3	E37, E38, E39	IC DEC 7408	1910135	32
3	E40, E41, E42	IC DEC 7408	1910135	31
1	E43, E44, E45	SCR DTL HD 2N12 2.5K 250V	9000671	30
1	E46, E47, E48, E49, E50, E51, E52, E53, E54, E55, E56, E57, E58, E59, E60, E61	IC DEC 7474	1909712	29
2	E43, E44	IC DEC 7474	1909712	28
1	E45	IC DEC 74123	1909337	27
1	E46	IC DEC 74123	1909337	26
3	E47, E48, E49	IC DEC 7400	1905525	25
1	E50	IC DEC 7413	1909989	24
1	E50, E53	IC DEC 7402	1909904	23
1	E3, E4, E33, E35, E40, E45, E46	IC DEC 5543	111464	22
1	E4	IC DEC 74123	1909337	21
1	E50	IC DEC 74108	1909953	20
1	E42	IC DEC 7492	1909053	19
4	E4, E11, E38, E39	IC DEC 7404	1909606	18
1	E24	IC DEC 7493	1909053	17
1	E25	IC DEC 7410	1905376	16
1	E26	IC DEC 510	1909712	15
1	E27	IC DEC 7425	1909712	14
1	E28	IC DEC 8271	1909712	13
2	E13, E14	IC DEC 7473	190631	12
1	E15	BRACKET CRYSTAL HOLDER	330285	11
1	E16	BRACKET CRYSTAL HOLDER	330285	10
1	E17	IC SOCKET	9000650	9
2	S1, S2	SWITCH, SINGLE POLE, 10 POS	1210012-1	8
1		40 PIN CONNECTOR BERG	1209911-12	7
1		SPLIT LOGS	900733	6
1		EXETER WASH-7 2.5 SYMPHON	900733	5
1		HANDLES, PLIP-CHIP MOUNT	9008357-06	4
1		ETCHED CIRCUIT BOARD	3000978	3
REF		MODULAR ECO HISTORY	3000978	2
REF		ASSY/DRILLING HOLE LAYOUT	1-444444-1	1
REF		XY COORDINATE HOLE	1-444444-2	1

DEC 74161	8	16	-	-
DEC 7380	1	8	-	-
DEC LMAT	3	1	-	2
DEC 74175	8	16	-	-
DEC 8271	8	16	-	-
DEC 7412	8	16	-	-
DEC 330	1	8	-	-
DEC 7423	10	5	-	-
DEC 7425	10	5	-	-
DEC 74123	8	16	-	-
DEC 8640	1	8	-	-
DEC 7413	10	5	-	-
DEC 7418	8	16	-	-
IC TRN	8	16	-	-



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DIGITAL EQUIPMENT CORPORATION



REV	DATE	BY
01	11/17/66	S. MASANO
02	11/17/66	R. HARRINGTON
03	11/17/66	P. HARRINGTON

FIRST USED ON OPTION/MODEL	QTY.	DESCRIPTION	PART NO.	ITEM NO.
DL11				
UNLESS OTHERWISE SPECIFIED DIMENSION IN INCHES TOLERANCES				
DECIMALS	ANGLES	TITLE ASYNCHRONOUS LINE INTERFACE (BUS RECEIVERS & DRIVERS) DL-2		
.XX - .008	± 30'	EQUIPMENT CORPORATION		
.XX - .02		MATERIAL		
.X - .1		FINISH		
MATERIAL		NEXT HIGHER ASST.		REV. K
FINISH		SCALE		NUMBER M7800-YA-1
		SHEET 2 OF 6		DIST.

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DIGITAL EQUIPMENT CORPORATION

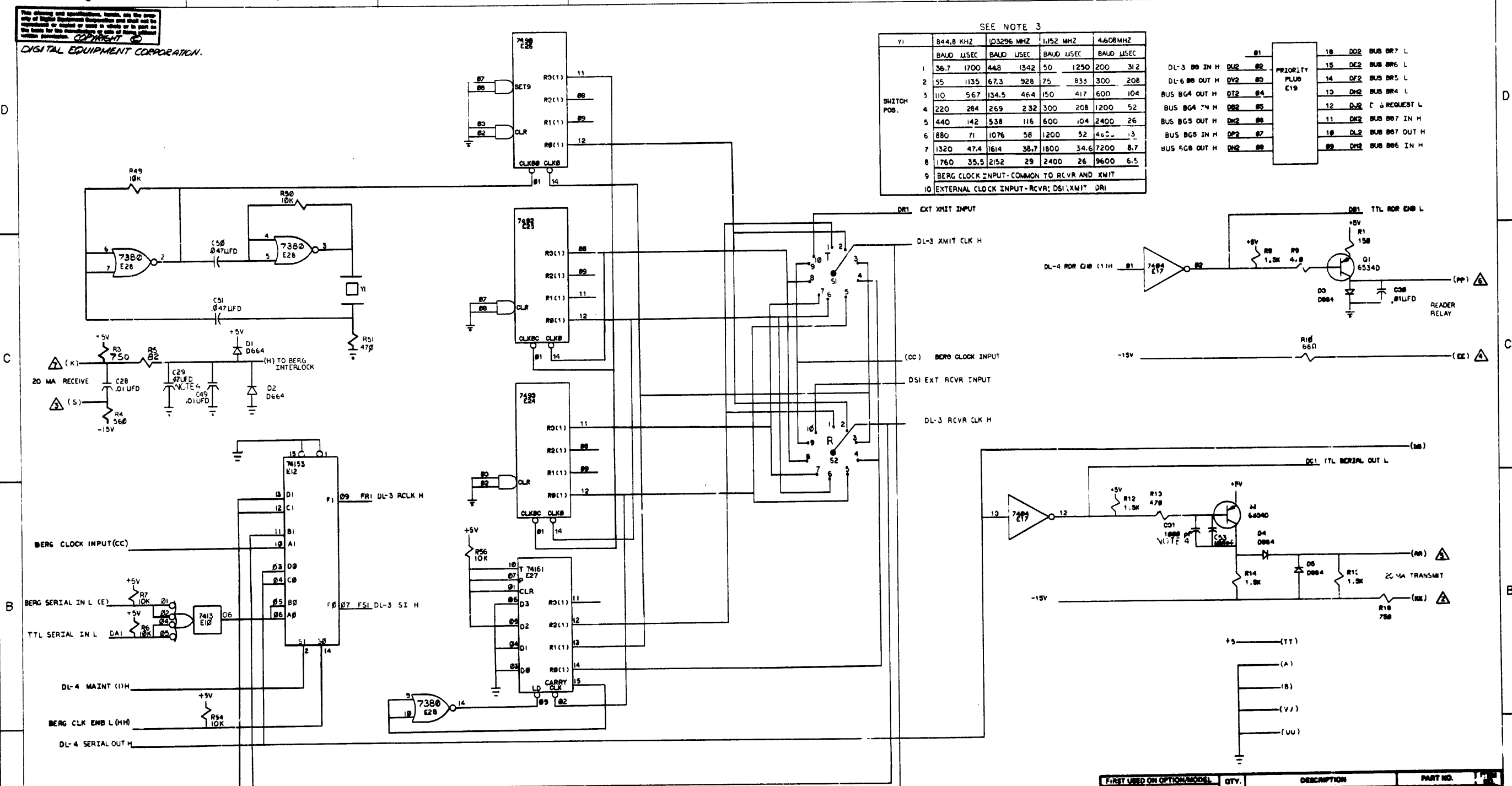
D

C

B

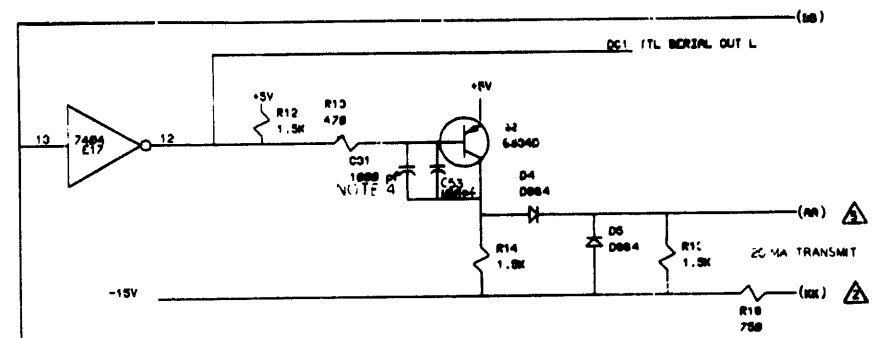
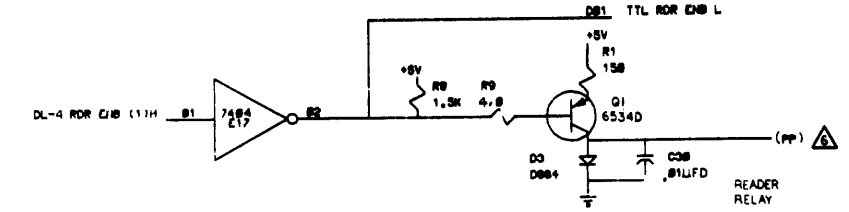
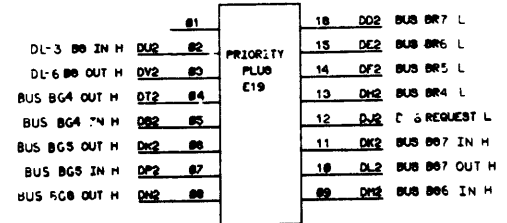
A

8 7 6 5 4 3 2 1



SEE NOTE 3

Y1	844.8 KHZ	103296 MHZ	1452 MHZ	4608MHZ
	BAUD U/SEC	BAUD U/SEC	BAUD U/SEC	BAUD U/SEC
1	36.7 1700	44.8 1342	50 1250	200 312
2	55 1135	67.3 928	75 833	300 208
3	110 567	134.5 464	150 417	600 104
4	220 284	269 232	300 208	1200 52
5	440 142	538 116	600 104	2400 26
6	880 71	1076 58	1200 52	4800 13
7	1320 47.4	1614 38.7	1800 34.6	7200 8.7
8	1760 35.5	2152 29	2400 26	9600 6.5
9	BERG CLOCK INPUT - COMMON TO RCVR AND XMIT			
10	EXTERNAL CLOCK INPUT - RCVR: DSI; XMIT: DRI			



- NOTES:
- LETTERS ENCLOSED IN PARENTHESIS REFER TO PINS ON THE BERG CONNECTOR, EXAMPLE: (X).
 - NUMBERS WITHIN TRIANGLES REFER TO PINS ON THE FEMALE MATE-N-LOCK CONNECTOR WHEN USING THE 7008360 CABLE. THIS CABLE ALSO CONNECTS BERG PINS H TO E.
 - ALTHOUGH THE ABOVE TABLE INCLUDES ONLY THE STANDARD DL11 CRYSTALS OTHER VALUES MAY BE SPECIFIED BY THE CUSTOMER OR BY OTHER DOCUMENTATION OF AN OPTION WHICH USES THE DL11.
 - REMOVE C29 AND C31 FOR OPERATION AT FREQUENCIES ABOVE 150 BAUD

FIRST USED ON OPTION/MODEL	QTY.	DESCRIPTION	PART NO.
DL11			
UNLESS OTHERWISE SPECIFIED DIMENSION IN INCHES TOLERANCES			
D.CIMALS			
XXX - .005			
.XX - .002			
.X - .001			
REMOVE BURRS AND BREAK SHARP CORNERS SURFACE QUALITY			
MATERIAL			
FINISH			

PARTS LIST		EQUIPMENT CORPORATION	
TITLE: ASYNCHRONOUS LINE INTERFACE (CLOCK & CURRENT LOOPS) DL-3			
REV. 000	NUMBER	REV. 000	NUMBER
DCS	M7800-YA-1	K	

D

C

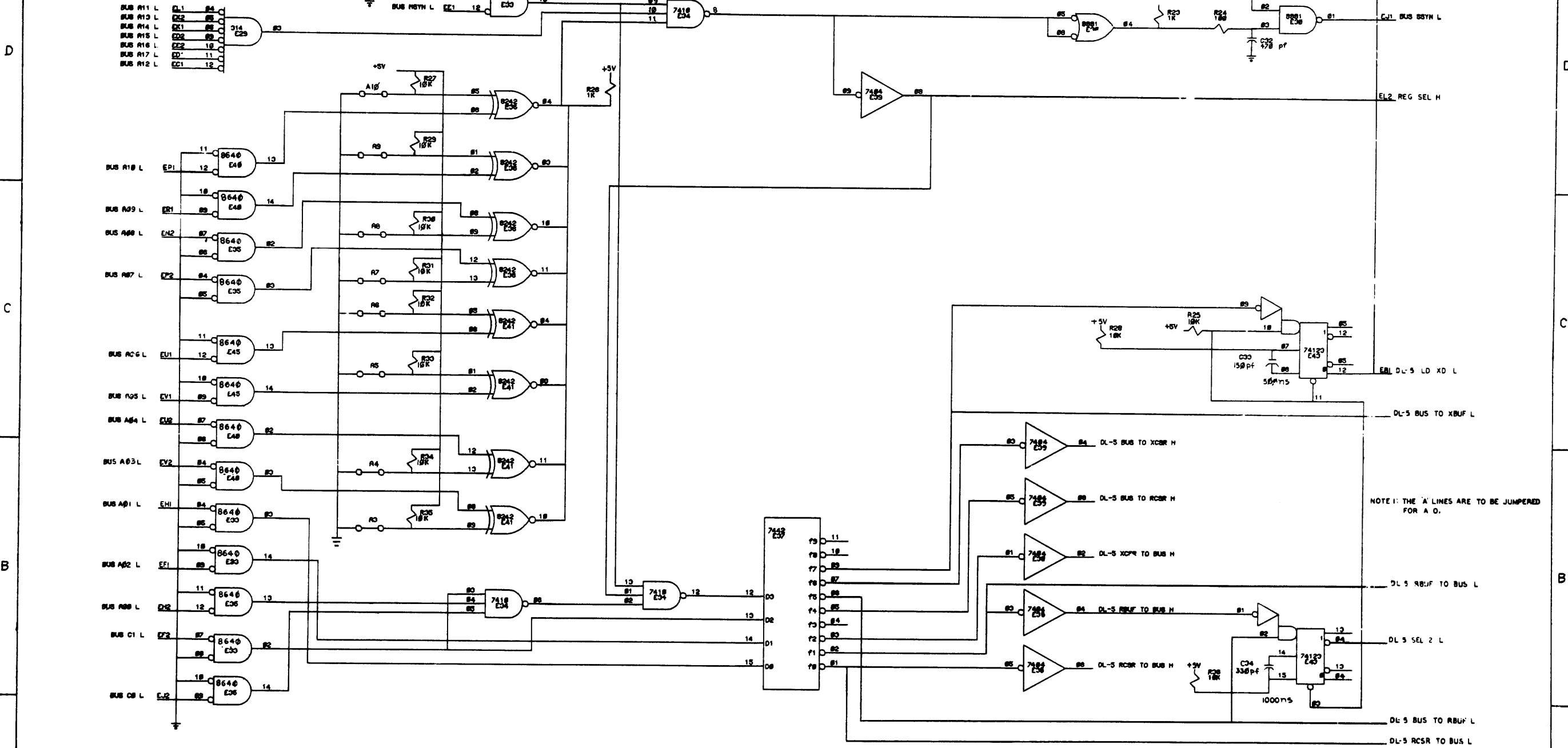
B

A

8 7 6 5 4 3 2 1 MK

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DIGITAL EQUIPMENT CORPORATION

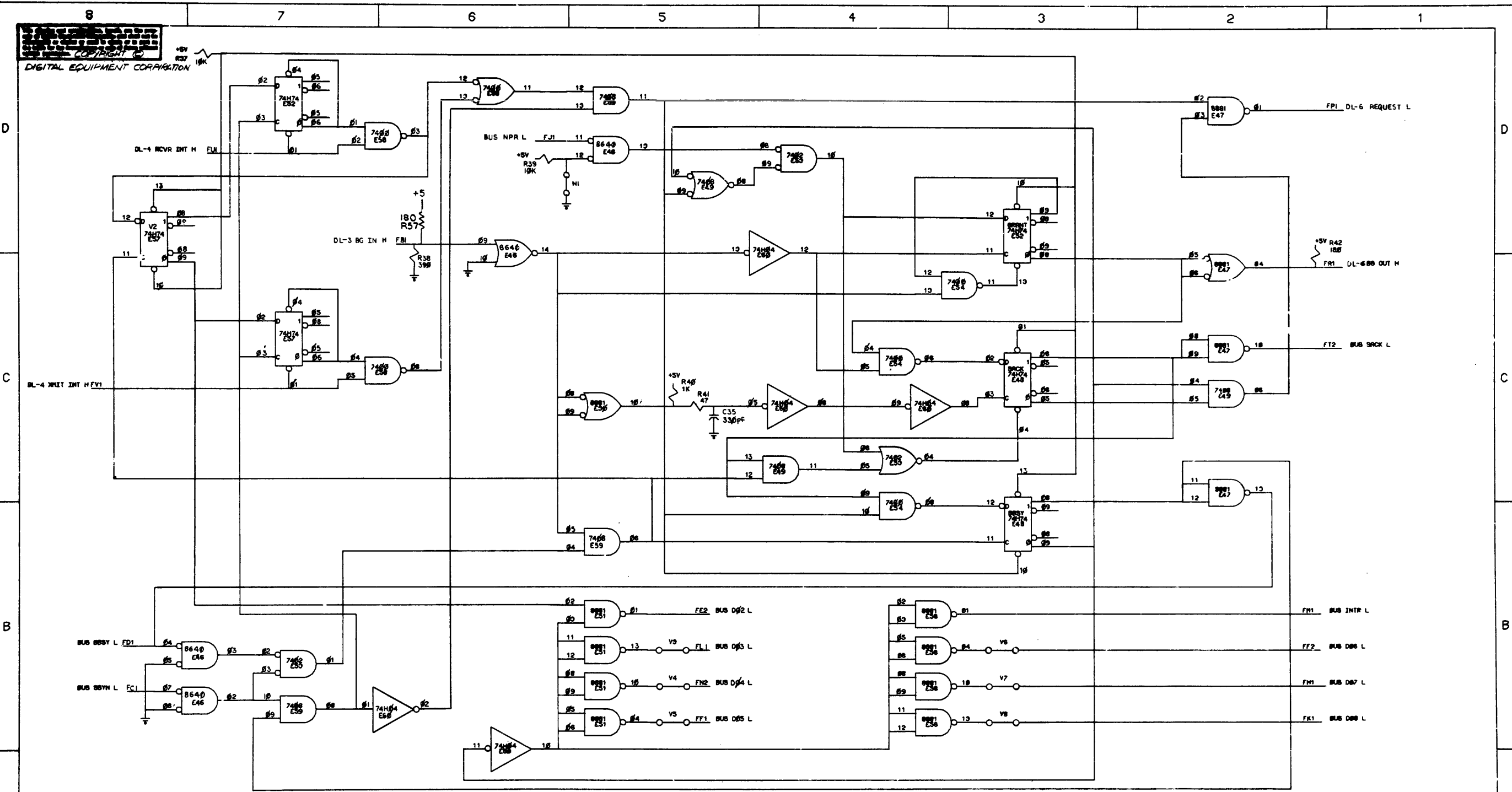


REVISIONS		
CHK.	CHANGE NO.	REV.

FIRST LEGEND OR OPTION MODEL	QTY.	DESCRIPTION	PART NO.
DL11			
UNLESS OTHERWISE SPECIFIED DIMENSION IN INCHES.			
TOLERANCES			
DECIMALS	ANGLES		
.XXX - .000	± 0° 30'		
.XX - .00			
.X - .1			
REMOVE BURRS AND BREAK SHARP CORNERS SURFACE QUALITY			
MATERIAL		TEXT HIGHER ASSY.	
FINISH		SCALE	
SHEET 5 OF 6			

EQUIPMENT CORPORATION
ASYNCHRONOUS LINE INTERFACE
(ADDRESS SELECTION)
DL-5

REV. 0000 NUMBER
 DCS M7800-YA-1 K
 DATE: 11/17/72
 DWT.



NOTE: THE 'V' LINES ARE TO BE JUMPED FOR A L.

REV.	DATE	BY	CHKD.	CRG. NO.	REV.

FIRST USED ON OPTION/MODEL	QTY.	DESCRIPTION	PART NO.	REV.
DL11				
UNLESS OTHERWISE SPECIFIED DIMENSION IN INCHES TOLERANCES				
DECIMALS		ANGLES		
.XXX - .000		±0° 30'		
.XX - .00				
.X - .0				
REMOVE BURRS AND BREAK SHARP CORNERS SURFACE QUALITY				
MATERIAL		NEXT HIGHER ASSY.		
FINISH		SCALE		
		SHEET 6 OF 6		

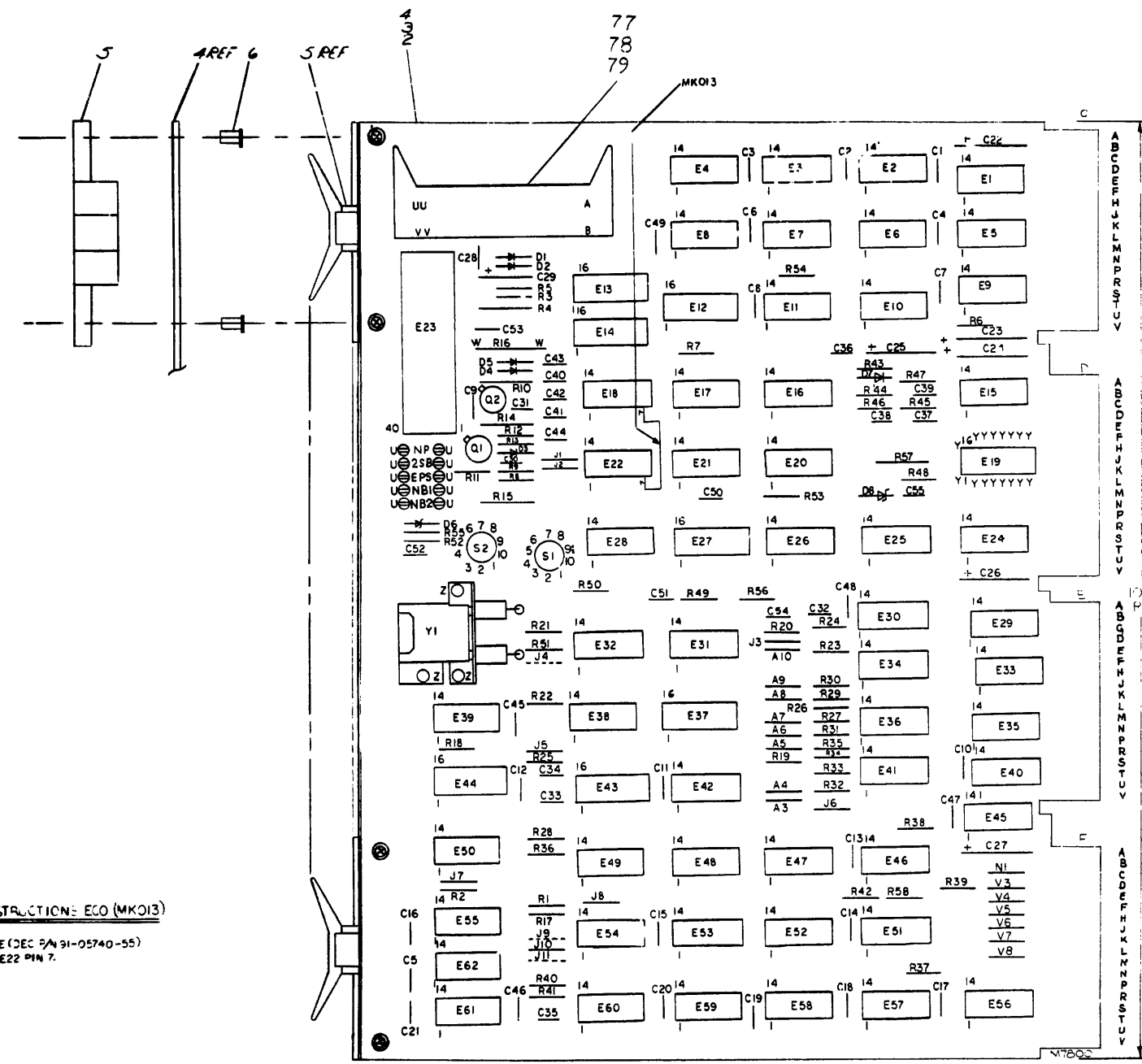
TITLE ASYNCHRONOUS LINE INTERFACE (INTERRUPT CONTROL) DL-6

DCS M7800-YA-1

1 MK

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- NOTES:**
- PIN NOTATION THROUGHOUT IS ORDERED UPON MODULE PLACEMENT IN THE SYSTEM UNIT. MODULE REFERENCE ALONE IS OBTAINED BY CONVERTING THE FIRST LETTER ACCORDING TO THE PIN NOMENCLATURE CHART AT THE LEFT.
 - NUMBERS TO BE USED AT CONNECTIONS A3-A10, J1-J10, V3-V8, AND N1.
 - LETTERS ENCLOSED IN PARENTHESIS REFER TO PINS ON THE BERG CONNECTOR. EXAMPLE: (X1).
 - DEC 86425 WERE PHASED IN AS DEC 388 REPLACEMENTS ANY 388 FAILURES SHOULD BE REPLACED BY 86425'S EXCEPT E28. E28 MUST BE REPLACED WITH A 7380.
 - FOR IC VERSION, C36 VALUE IS 1200PF.

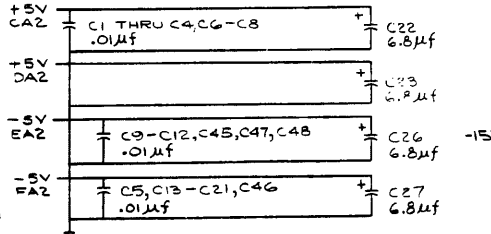


PIN NOMENCLATURE
MODULE SYSTEM UNIT

QTY	REF DESIGNATION	DESCRIPTION	PART NO.	NO.
1	F78	IC DEC 7380	1910380	50
1		LATCH RIGHT	1209941-04	79
1		LATCH LEFT	1209941-03	78
1		CONNECTOR BERG	1209941-02	77
1		INSULATED JUMPER	8009185	76
1	C36	CAP 1200 PF, 100V 5%	1002424	75
1	C36	WIRE * 22 SOLID 6/US	9107560/01	74
1	R27	RES 33, 1/4W, 5%	1302336	73
1	C8	DIODE, 4742, 12V, 10%, 1W, ZENKER	1101502	72
1		INT. WASHER # 2	9006831	71
1	E23	IC DEC 74161	1910830	70
1		NUT, HEX # 2	9006835	69
1		SCR PH. PAN HD 256X1/16	9006601-1	68
1		AUGAT 8000 PG-1	1202812	67
1	D1, D5, D7	DIODE DGG4 20V 100MA	1102114	66
1	D2	DIODE IN 74 GA	1103186	65
1	C42	TRANSISTOR C85 4AD	1103190	64
1	C53	CAP 100PF, 100V 5% TANT	1000016	63
1	C54	CAP 560PF, 100V 5% TANT	1000008	62
1	C55	CAP .047 MF CERAMIC	1000678	61
1	C56	CAP 220PF 100V 5% TANT	1000001	60
1	C34, C35	CAP 330PF 100V 5% TANT	1000003	59
1	C32, C37, C44	CAP 100PF 100V 5% TANT	1000002	58
1	C31	CAP 100PF 100V 5% TANT	1000002	57
1	C30	CAP 100PF 100V 5% TANT	1000002	56
1	C29	CAP 100PF 100V 5% TANT	1000002	55
1	C27	CAP 100PF 100V 5% TANT	1000002	54
1	C28	CAP 100PF 100V 5% TANT	1000002	53
1	R45	RES 1.5K 1/4W 5%	1300534	52
1	R46	RES 1.5K 1/4W 5%	1300534	51
1	R47	RES 1.5K 1/4W 5%	1300534	50
1	R48	RES 1.5K 1/4W 5%	1300534	49
1	R49	RES 1.5K 1/4W 5%	1300534	48
1	R50	RES 1.5K 1/4W 5%	1300534	47
1	R51	RES 1.5K 1/4W 5%	1300534	46
1	R52	RES 1.5K 1/4W 5%	1300534	45
1	R53	RES 1.5K 1/4W 5%	1300534	44
1	R54	RES 1.5K 1/4W 5%	1300534	43
1	R55	RES 1.5K 1/4W 5%	1300534	42
1	R56	RES 1.5K 1/4W 5%	1300534	41
1	R57	RES 1.5K 1/4W 5%	1300534	40
1	R58	RES 1.5K 1/4W 5%	1300534	39
1	R59	RES 1.5K 1/4W 5%	1300534	38
1	R60	RES 1.5K 1/4W 5%	1300534	37
1	R61	RES 1.5K 1/4W 5%	1300534	36
1	R62	RES 1.5K 1/4W 5%	1300534	35
1	R63	RES 1.5K 1/4W 5%	1300534	34
1	R64	RES 1.5K 1/4W 5%	1300534	33
1	R65	RES 1.5K 1/4W 5%	1300534	32
1	R66	RES 1.5K 1/4W 5%	1300534	31
1	R67	RES 1.5K 1/4W 5%	1300534	30
1	R68	RES 1.5K 1/4W 5%	1300534	29
1	R69	RES 1.5K 1/4W 5%	1300534	28
1	R70	RES 1.5K 1/4W 5%	1300534	27
1	R71	RES 1.5K 1/4W 5%	1300534	26
1	R72	RES 1.5K 1/4W 5%	1300534	25
1	R73	RES 1.5K 1/4W 5%	1300534	24
1	R74	RES 1.5K 1/4W 5%	1300534	23
1	R75	RES 1.5K 1/4W 5%	1300534	22
1	R76	RES 1.5K 1/4W 5%	1300534	21
1	R77	RES 1.5K 1/4W 5%	1300534	20
1	R78	RES 1.5K 1/4W 5%	1300534	19
1	R79	RES 1.5K 1/4W 5%	1300534	18
1	R80	RES 1.5K 1/4W 5%	1300534	17
1	R81	RES 1.5K 1/4W 5%	1300534	16
1	R82	RES 1.5K 1/4W 5%	1300534	15
1	R83	RES 1.5K 1/4W 5%	1300534	14
1	R84	RES 1.5K 1/4W 5%	1300534	13
1	R85	RES 1.5K 1/4W 5%	1300534	12
1	R86	RES 1.5K 1/4W 5%	1300534	11
1	R87	RES 1.5K 1/4W 5%	1300534	10
1	R88	RES 1.5K 1/4W 5%	1300534	9
1	R89	RES 1.5K 1/4W 5%	1300534	8
1	R90	RES 1.5K 1/4W 5%	1300534	7
1	R91	RES 1.5K 1/4W 5%	1300534	6
1	R92	RES 1.5K 1/4W 5%	1300534	5
1	R93	RES 1.5K 1/4W 5%	1300534	4
1	R94	RES 1.5K 1/4W 5%	1300534	3
1	R95	RES 1.5K 1/4W 5%	1300534	2
1	R96	RES 1.5K 1/4W 5%	1300534	1

REWORK INSTRUCTIONS: ECO (MKO13)
WIREADD:
ADD 30 GAUGE WIRE (DEC 74N 91-05740-55)
FROM E18 PIN 7 TO E22 PIN 7.

DEC 74111	8	16	-	-
DEC 1400	7	-	14	1
DEC 1447	3	1	-	2
DEC 74115	8	16	-	-
DEC 8671	8	16	-	-
DEC 74112	8	16	-	-
DEC 314	1	8	-	-
DEC 74113	10	5	-	-
DEC 74114	10	5	-	-
DEC 74115	8	16	-	-
DEC 8640	1	8	-	-
DEC 74110	10	5	-	-
DEC 74113	8	16	-	-



DEC NO.	ISA NO.	DEC NO.	ISA NO.
68340	WPS6834	INT7464	114M 33A
0464	1A3624		

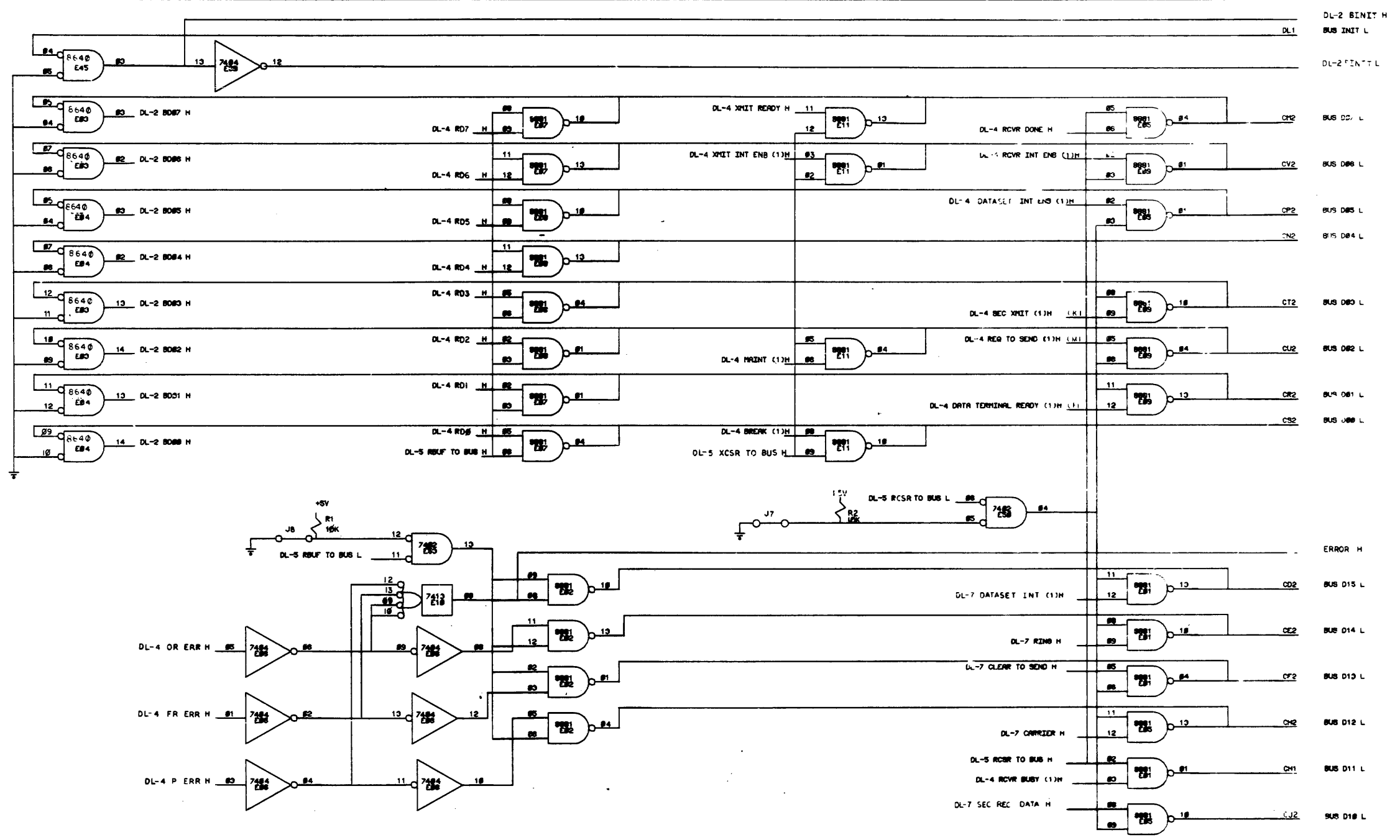
SEMICONDUCTOR CONVERSION CHART

ONE AND PIN ARE USUALLY PIN 7 AND 14 RESPECTIVELY EXCEPT AS NOTED ABOVE

IC PIN LOCATIONS

IC TYPE	8	16	+	+	-12V

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REV	DATE	BY	CHKD

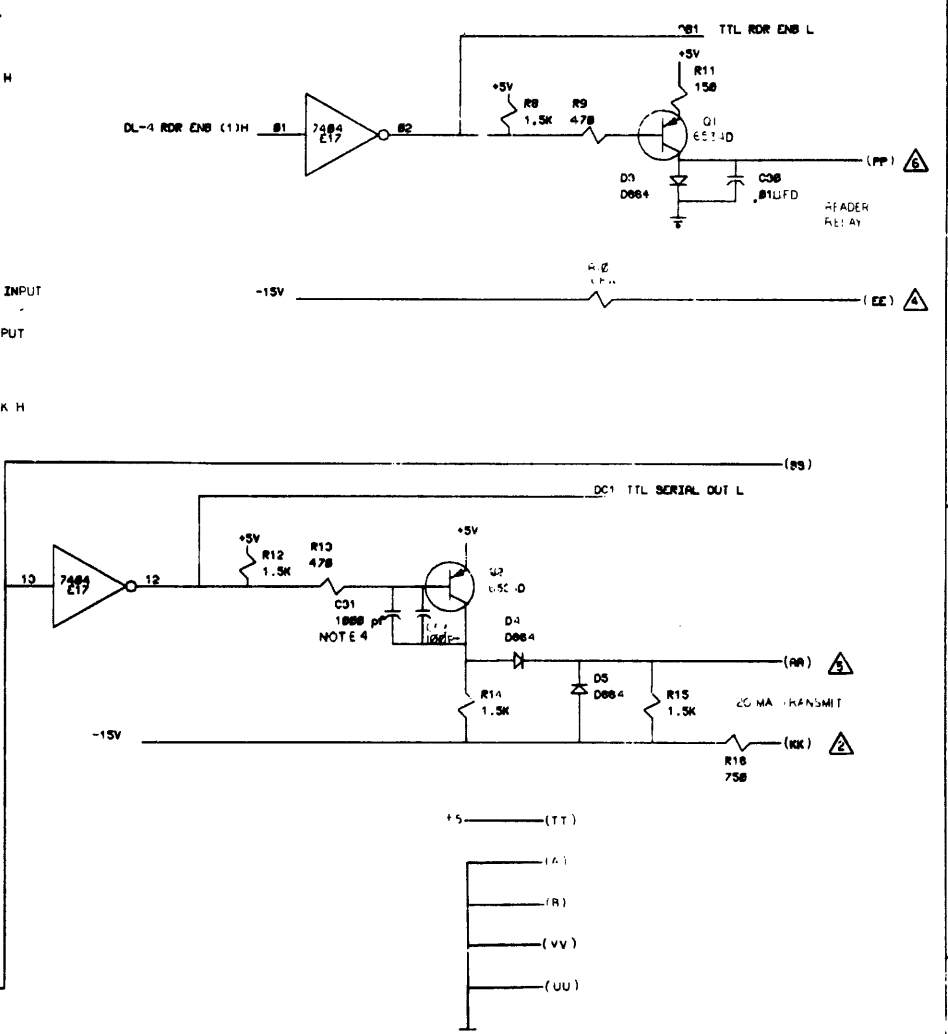
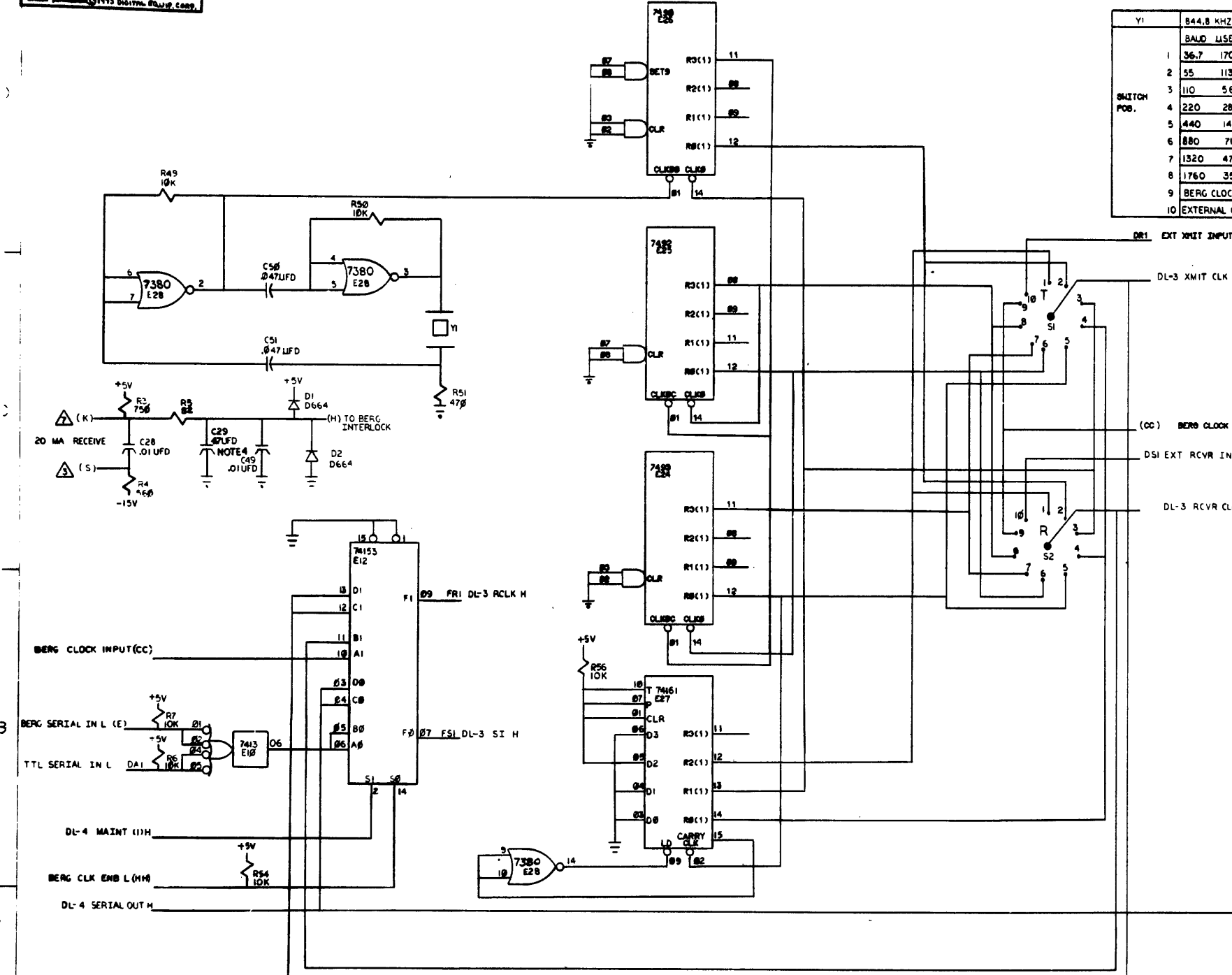
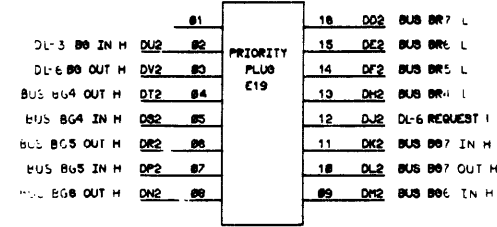
FIRST USED OR OPTION NO.	QTY.	DESCRIPTION	PART NO.
DL11			

UNLESS OTHERWISE SPECIFIED DIMENSIONS IN INCHES TOLERANCES	DATE: 3-20-71	EQUIPMENT CORPORATION (BUS RECEIVERS & DRIVERS)
DECIMALS .XX - .01 .XXX - .005 .X - .1	DATE: 1/27/72 DATE: 3-2-72 DATE: 1-17-71	
REMOVE BURRS AND BREAK SHARP CORNERS SURFACE QUALITY	MATERIAL: 74LS00	TITLE: ASYNCHRONOUS LINE INTERFACE (BUS RECEIVERS & DRIVERS)
FINISH: //	SCALE: 1/8"	DCS M7000-0-1

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SEE NOTE 3

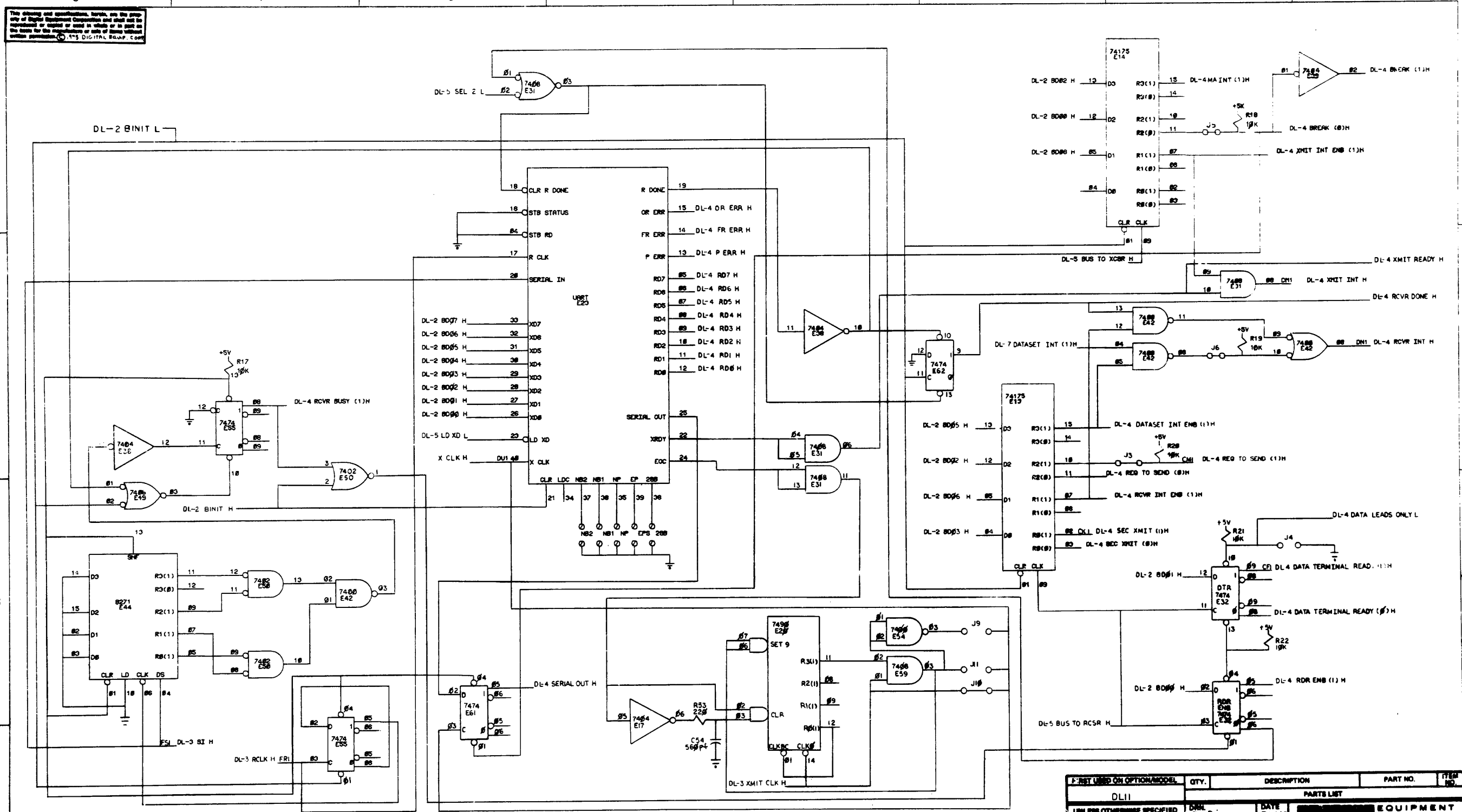
Y1	844.8 KHZ	103296 MHZ	1.152 MHZ	4.608MHZ
	BAUD USEC	BAUD USEC	BAUD USEC	BAUD USEC
1	36.7	1700	44.8	1342
2	55	1135	67.3	928
3	110	567	134.5	464
4	220	284	269	232
5	440	142	538	116
6	880	71	1076	58
7	1320	47.4	1614	38.7
8	1760	35.5	2152	29
9	BERG CLOCK INPUT-COMMON TO RCVR AND XMIT			
10	EXTERNAL CLOCK INPUT-RCVR; DSI; XMIT DRI			



- NOTES:
- LETTERS ENCLOSED IN PARENTHESIS REFER TO PINS ON THE BERG CONNECTOR, EXAMPLE: (X).
 - NUMBERS WITHIN TRIANGLES REFER TO PINS ON THE FEMALE MATE-N-LOCK CONNECTOR WHEN USING THE 700360 CABLE, THIS CABLE ALSO CONNECTS BERG PINS H TO E.
 - ALTHOUGH THE ABOVE TABLE INCLUDES ONLY THE STANDARD DLII CRYSTALS OTHER VALUES MAY BE SPECIFIED BY THE CUSTOMER OR BY OTHER DOCUMENTATION OF AN OPTION WHICH USES THE DLII.
 - C29 AND C31 ARE REQUIRED ONLY FOR 20MA OPERATION AT 150BAUD OR LESS, DLII-A OR C.

FIRST USED ON OPTION/MODEL	QTY.	DESCRIPTION	PART NO.	FIN. NO.
DLII		PARTS LIST		
UNLESS OTHERWISE SPECIFIED DIMENSION IN INCHES TOLERANCES	DWG. DATE 8-7-72	DATE 8-5-72	EQUIPMENT CORPORATION	
DECIMALS ANGLES	CHK'D. DATE 8-5-72	DATE 8-5-72	TITLE ASYNCHRONOUS LINE INTERFACE	
.XXX = .000 .XX = .00 .X = .01	ISS. DATE 8-5-72	DATE 8-5-72	CLOCK & CURRENT LOOPS DL-3	
REMOVE BURRS AND BREAK SHARP CORNERS SURFACE QUALITY	PROG. DATE 8-5-72	DATE 8-5-72	MATERIAL NEXT HIGHER ASSY.	
FINISH	SCALE	REV.	D CS	M7800-0-1
	SHEET 3 OF 7	DIST.		

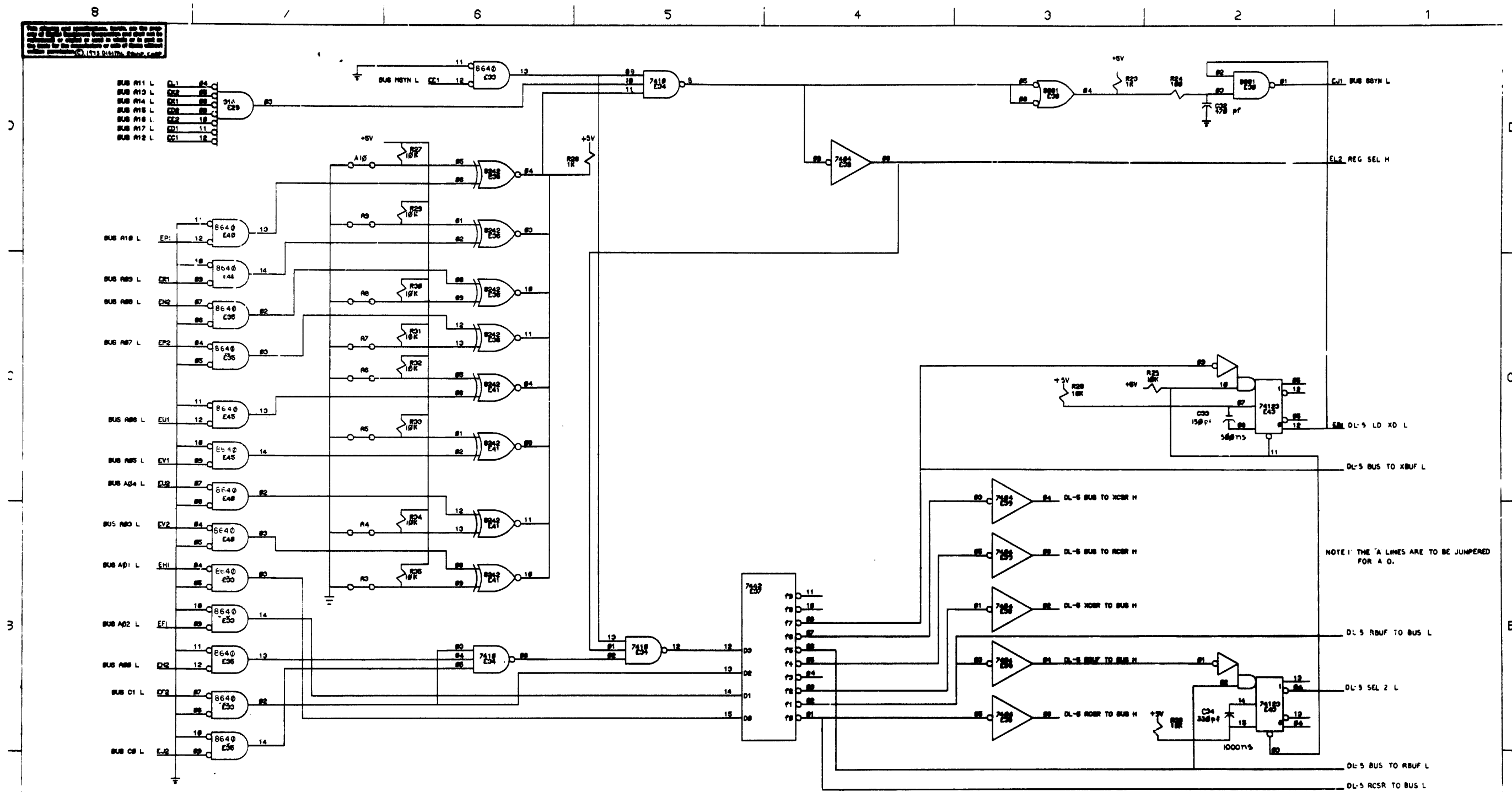
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REVISONS		
CHR.	CHANGE NO.	REV.

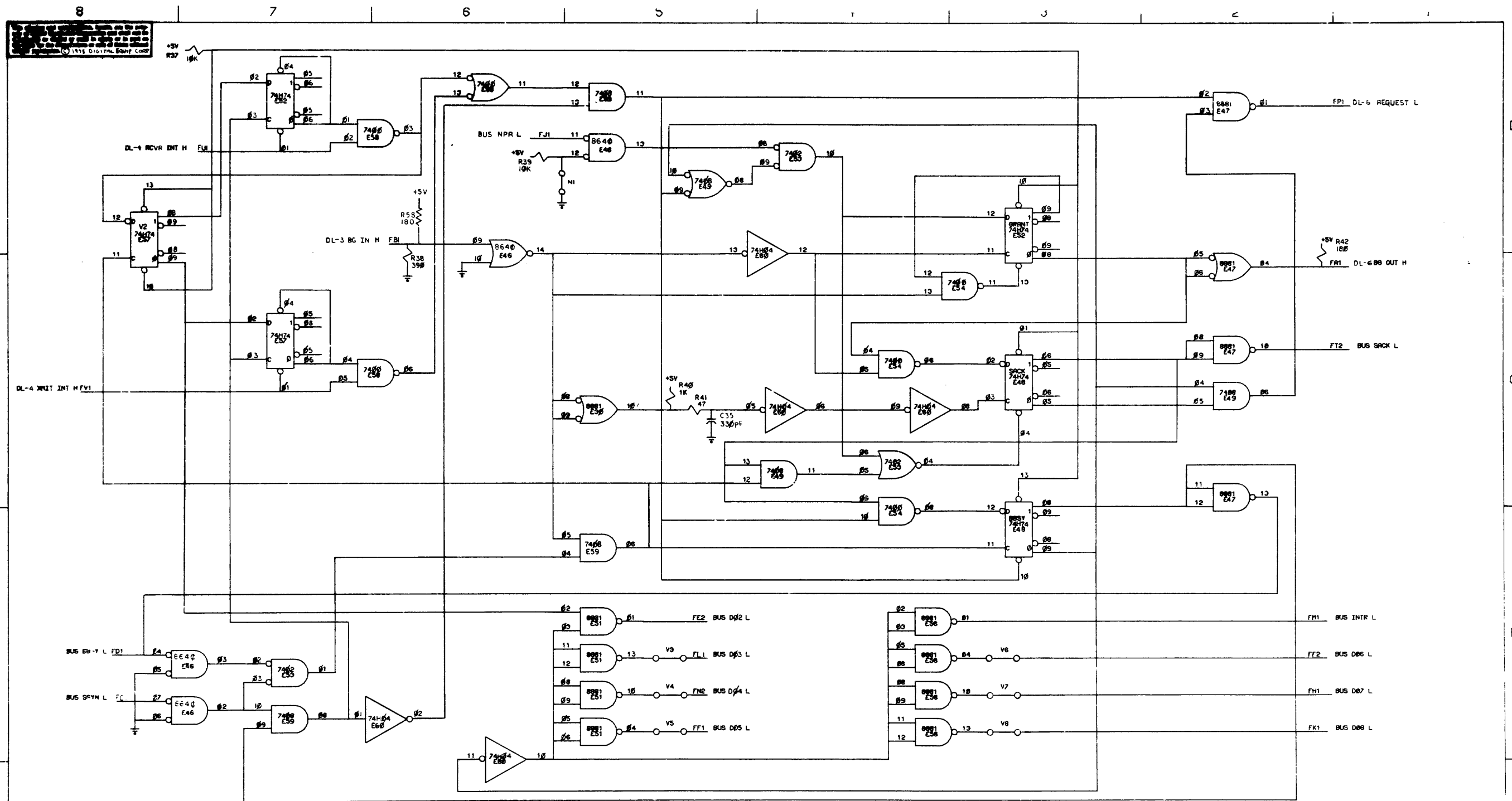
PART USED OR OPTION/MODEL		QTY.	DESCRIPTION	PART NO.	ITEM NO.
DL11					
UNLESS OTHERWISE SPECIFIED DIMENSION IN INCHES TOLERANCES		DATE	EQUIPMENT CORPORATION		
DECIMALS		CHK'D	DATE		
ANGLES		DATE	DATE		
.XXX = .008		DATE	DATE		
.XX = .002		DATE	DATE		
.X = .001		DATE	DATE		
REMOVE BURRS AND BREAK SHARP CORNERS SURFACE QUALITY		DATE	DATE		
MATERIAL		NEXT HIGHER ASSEMBY	SIZE CODE	NUMBER	REV.
FINISH		SCALE	DCS	M7800-0-1	R
SHEET		OF	DST.		

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REV.	DESCRIPTION	REV.

PART NUMBER	QTY.	DESCRIPTION	PART NO.	REV.
DL11				
PARTS LIST				
UNLESS OTHERWISE SPECIFIED				
DIMENSIONS IN INCHES				
TOLERANCES				
DECIMALS	ANGLES	DATE		
XXX - .005	± 0° 30'	E. J. Pender 10-20-73		
XX - .010		J. G. Pender 10-27-73		
X - .015		E. J. Pender 10-27-73		
REMOVE BURRS AND BREAK SHARP CORNERS SURFACE QUALITY				
MATERIAL				
FINISH				
EQUIPMENT CORPORATION				
TITLE: ASYNCHRONOUS LINE INTERFACE (ADDRESS SELECTION) DL-5				



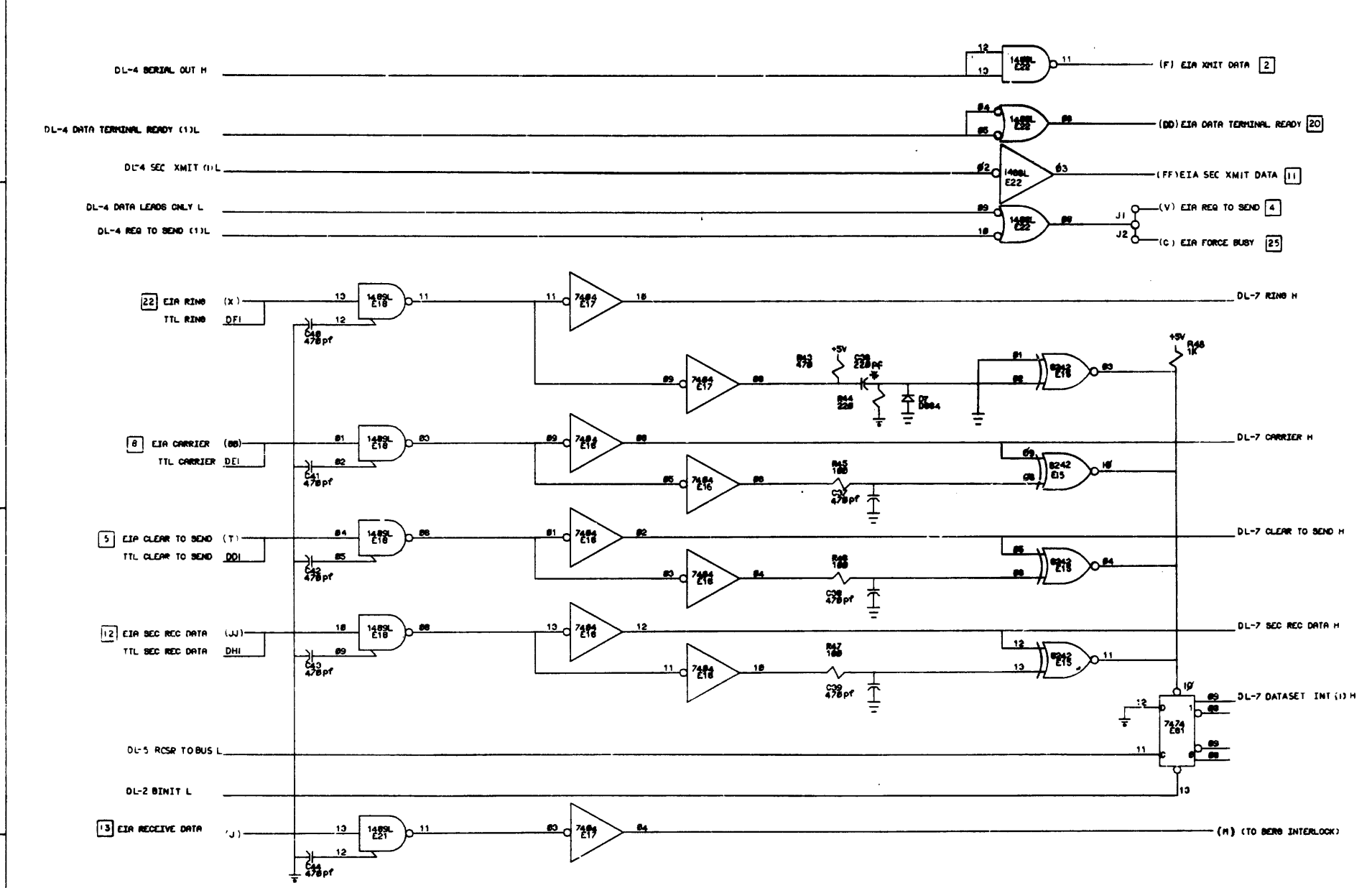
NOTE: THE V LINES ARE TO BE JUMPED FOR A L.

REV.	DATE	BY	CHKD.

FIRST USED ON OPTION/MODEL	QTY.	DESCRIPTION	PART NO.	ITEM NO.
DL11				
PARTS LIST				
UNLESS OTHERWISE SPECIFIED DIMENSION IN INCHES. TOLERANCES	DRAWN B.P. 10/22	DATE 5-20-72	EQUIPMENT CORPORATION SOFTWARE, HARDWARE, SYSTEMS	
DECIMALS .XXX = .005 .XX = .02 .X = .1	CHKD. J. M. 10/22	DATE 9-6-72		
REMOVE BURRS AND BREAK SHARP CORNERS SURFACE QUALITY	ENG. J. M. 10/22	DATE 4-27-72	TITLE ASYNCHRONOUS LINE INTERFACE (INTERRUPT CONTROL) DL-6	
MATERIAL 	PRD. J. M. 10/22	DATE 2-7-72		
FINISH 	NEXT HIGHER ASSY. 		ALZ. CODE DCS	NUMBER M7800-0-1
	SCALE 			REV. R
	SHEET 	OF 		

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D
C
B
A



FOR YC VERSION, C34 VALUE CHANGES TO 1200PF

- NOTES:
 1. LETTERS ENCLOSED-EXAMPLE (M) REFER TO PINS ON THE BERG CONNECTOR.
 2. NUMBERS WITHIN BOXES REFER TO PINS ON THE MALE CINCH CONNECTOR WHEN USING THE BC05-C CABLE. THIS CABLE ALSO CONNECTS BERG PINS H TO E.

REV.	DATE	BY

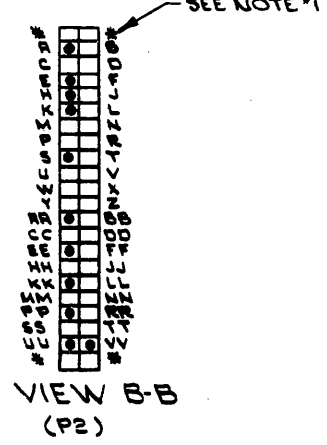
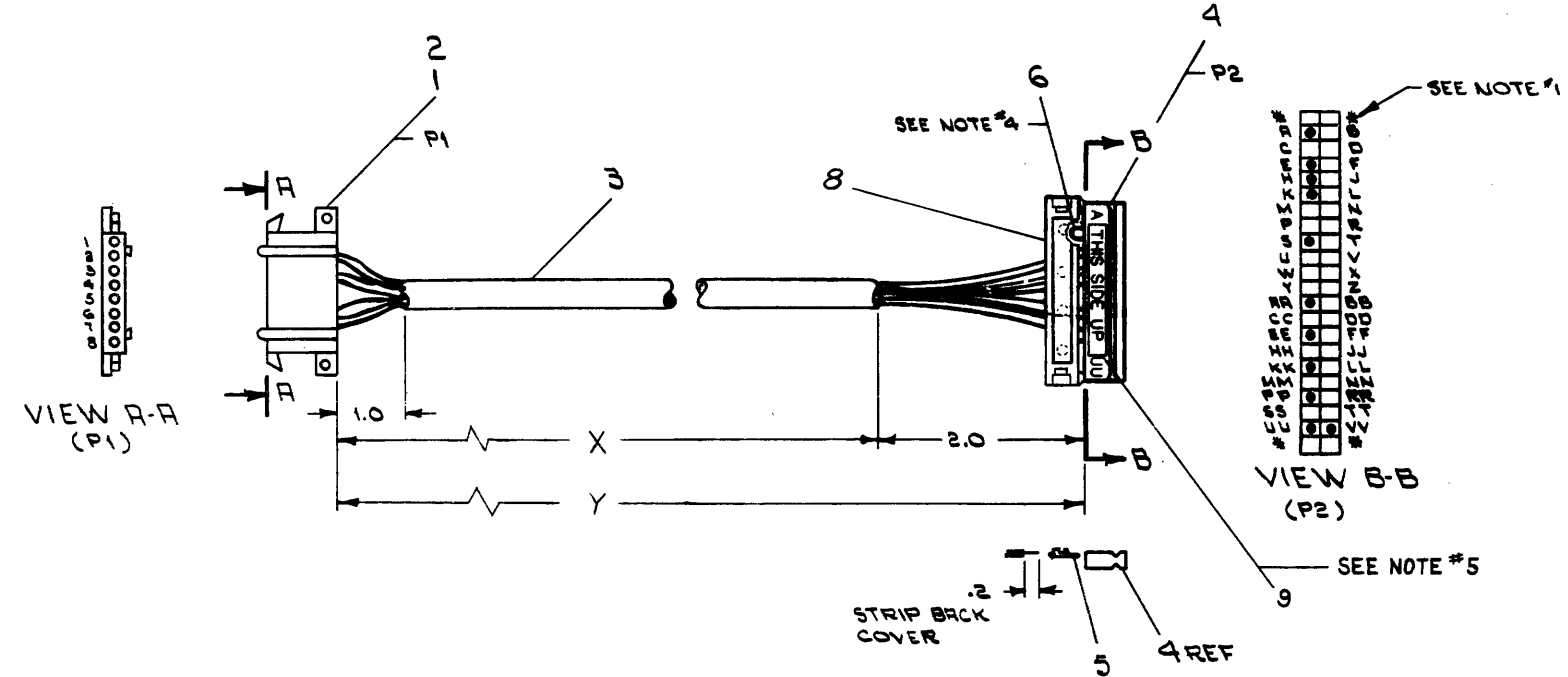
FIRST USED ON OPTION/MODEL	QTY.	DESCRIPTION	PART NO.	ITEM NO.
DL7				
PARTS LIST				
UNLESS OTHERWISE SPECIFIED DIMENSION IN INCHES. TOLERANCES	DATE 5-70-72	DATE 9-5-72	EQUIPMENT CORPORATION <small>(A DIVISION OF DIGITAL EQUIPMENT CORPORATION)</small> TITLE ASYNCHRONOUS LINE INTERFACE (EIA DRIVERS & RECEIVERS) DL-7	
DECIMALS	DATE 10-27-72	DATE 6-27-73		
ANGLES	DATE 10-27-72	DATE 6-27-73		
.XXX = .000 .XX = .02 .X = .1	DATE 10-27-72	DATE 6-27-73		
REMOVE BURRS AND BREAK SHARP CORNERS SURFACE QUALITY	DATE 10-27-72	DATE 6-27-73	REV. 001	REV. R
MATERIAL	NEXT HIGHER ASBY.	SCALE	D CS M7800-0-1	
FINISH		SHEET 7 OF 7	DIST.	

R 7 6 5 4 3 2 1

WIRE TABLE							
ITEM NO.	AWG	COLOR	PAIR NO.	FROM		TO	
				CONNECTION	WITH	CONNECTION	WITH
3	22	BLK	1	P1-2	2	P2-KK	5
3	1	RED		P1-3	2	P2-S	
3,7		SHIELD		SEE NOTE #2	-	P2-R(NOTE#3)	
3		BLK	2	P1-4	2	P2-EE	
3		WHT		P1-5	2	P2-RR	
3,7		SHIELD		SEE NOTE #2	-	P2-UU(NOTE#3)	
3		BLK	3	P1-6	2	P2-PP	
3		GRN		P1-7	2	P2-K	
3,7		SHIELD		SEE NOTE #2	-	P2-VV(NOTE#3)	
6	22	BLK	-	P2-E	3	P2-H	5

VARIATION	LENGTH	
	X	Y
	7008360-0	25IN±1.0
7008360-1	46IN±1.0	48IN±1.0
7008360-9	9FT±2IN	9FT2IN±2IN

- NOTES:**
- * ASTERISKS INDICATE CAVITIES NOT USED OR DESIGNATED BY LETTERS.
 - DRAIN WIRES TO BE CUT BACK TO OUTER INSULATION ON P1 END OF CABLE ONLY. SHIELDS TO BE CUT BACK TO OUTER INSULATION ON BOTH ENDS OF CABLES.
 - DRAIN WIRES ON P2 END OF CABLE TO BE EACH ENCLOSED WITH ITEM #7 (TUBING) FROM END OF CABLE JACKET TO POINT WHERE THEY ENTER P2 CONNECTOR.
 - ITEM #6 (WIRE) TO BE APPROXIMATELY ONE (1) INCH LONG.
 - PLACE ITEM #9 ("THIS SIDE UP" STICKER) ON LETTERED SIDE OF ITEM #4 (BERG HOUSING) AS SHOWN.



QTY.	DESCRIPTION	PART NO.	ITEM NO.
1	LABEL, THIS SIDE UP	3611567	9
1	STRAIN RELIEF	1211166	8
	R/R TUB. #8 TEF. THINWALL WRT	3NOTES-11	7
	R/R WIRE #22 AWG STRD TEF BLK	9107350-00	6
11	SOCKET, CRIMP #47216	1210089-07	5
1	HOUSING, BERG #65043-015	1210918-15	4
	R/R CABLE BELDEN #107T-3PR SHLD	9107T23-0	3
6	CONTACT WRT-N-LOCK (FEMALE)	1208379-03	2
1	CONN. WRT-N-LOCK (FEMALE)	1208340-00	1

REV.	DATE	BY	CHKD.	DESCRIPTION
A	10/27/74	J. MCNAMARA		REVISED TO ADD P1 AND P2 CONNECTIONS
B	10/27/74	J. MCNAMARA		REVISED TO ADD P1 AND P2 CONNECTIONS
C	10/27/74	J. MCNAMARA		REVISED TO ADD P1 AND P2 CONNECTIONS
D	10/27/74	J. MCNAMARA		REVISED TO ADD P1 AND P2 CONNECTIONS

FIRST USED ON OPTION / MODEL: PDP-8E

DO NOT SCALE DRAWING

UNLESS OTHERWISE SPECIFIED DIMENSION IN INCHES

TOLERANCES: ANGLES ± 0.5°

FINAL SURFACE QUALITY: REMOVE BURRS AND BREAK SHARP CORNERS

MATERIAL: SEE PARTS LIST

FINISH: SCALE NONE

DATE: 10/27/74

BY: J. MCNAMARA

CHKD.: J. MCNAMARA

APPROVED: J. MCNAMARA

EQUIPMENT CORPORATION
MAYFIELD, MASSACHUSETTS

TITLE: CABLE ASSEMBLY (KL8E)

NUMBER: DIA 7008360-0-0

SHEET: 1 OF 1

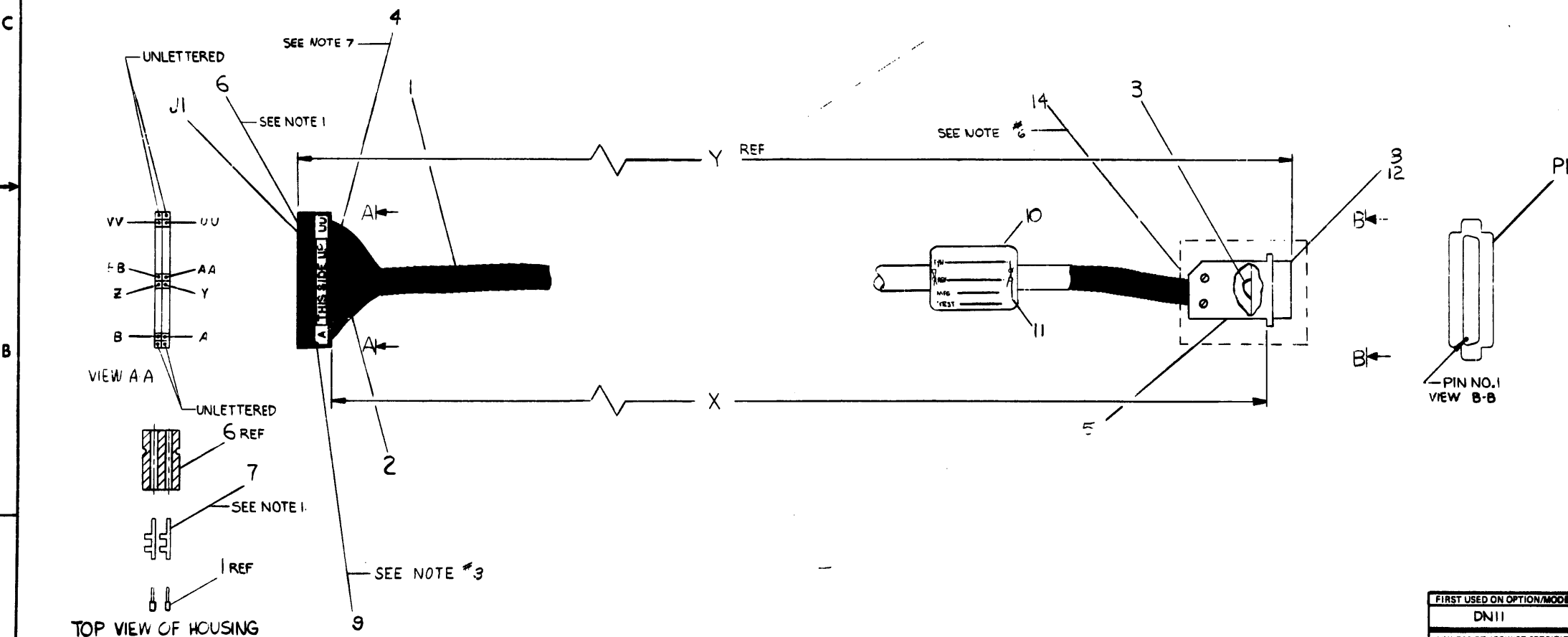
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WIRE TABLE													
ITEM NO.	AWG	COLOR	FROM		TO		ITEM NO.	AWG	COLOR	FROM		TO	
			CONNECTION	WITH	CONNECTION	WITH				CONNECTION	WITH		
1	26	BLU/WHT	PI-1	* 12	J1-VV	7	1	26	RED/BRN	PI-16	8	J1-NN	7
		WHT/BLU	PI-2	8	J1-F				SLA	PI-17		J1-R	
		ORN/WHT	PI-3		J1-J				RED/SLA	PI-18		J1-U	
		WHT/ORN	PI-4		J1-V				BLU/BLK	PI-19		J1-P	
		GRN/WHT	PI-5		J1-T				BLK/BLU	PI-20		J1-DD	
		WHT/GRN	PI-6	8	J1-Z				ORN/BLK	PI-21		J1-MM	
		BRN/WHT	PI-7	** 12	J1-UU				BLK/ORN	PI-22		J1-X	
		WHT/BRN	PI-8	8	J1-BB				GRN/BLK	PI-23		J1-RR	
		SLA/WHT	PI-9		J1-Y				BRN/RED	PI-24		J1-L	
		WHT/SLA	PI-10		J1-W				RED/ORN	PI-25	8	J1-C	
		BLU/RED	PI-11		J1-FF				SHIELD	PI-1	* 12	J1-A	
		RED/BLU	PI-12		J1-JJ				SHIELD	PI-7	** 12	J1-B	7
		ORN/RED	PI-13		J1-D				BLK	PI-1	* 12	PI-7 ** 12	
		SLA/RED	PI-14		J1-LL				RED	J1-E	7	J1-M	7
1	26	SLA/GRN	PI-15	8	J1-N								

NUMBER	VARIATION	
	DIM X	DIM Y (PRECUT)
BC05C-25	25'±3"	25'1.8"
BC05C-50	50'±2%	50'1.8"
BC05C-09	9'±3"	9'1.8"
BC05C-1F	18'±1"	19.5'

- NOTES:**
- MANUFACTURING SHOULD USE MACHINE CRIMPER TOOL FOR CRIMPING PINS (ITEM #7) MUST BE HT68 FROM BERG ELECT
 - ONLY DEC PART #1210918-15 MAY BE USED AS J1.
 - PLACE ITEM #9 ("THIS SIDE UP" STICKER) ON LETTERED SIDE OF ITEM #6 (BERG HOUSING) AS SHOWN.
 - USE ITEM #13 (NOT 302-11) IN TWO PLACES (PI-1, PI-7) TO PREVENT SHORTING
 - USE ITEM #2 (NOT 205-11) ON ALL REMAINING SOLDER CUPS TO PREVENT SHORTING
 - DUE TO ± TOLERANCES WITH DIFFERENT VENDORS THE HOOD (ITEM #5) MAY VARY IN OUTSIDE DIAMETER CAUSING POTENTIAL STRAW RELIEF GRIPPING PROBLEM. SHOULD THIS CONDITION BE PRESENT USE ITEM #4 (907834) AT JUNCTION OF CABLE AND HOOD.
 - PLACE ITEM #4 (9107256) OVER SHIELD WIRE J1-A, J1-R, PI-1, PI-7.

NB * DENOTES THREE WIRES ARE SOLDERED INTO THE PI-1 SOLDER CUP
 ** DENOTES THREE WIRES ARE SOLDERED INTO THE PI-7 SOLDER CUP



QTY.	DESCRIPTION	PART NO.	TYPE
1	HOOD	1210493-50	18
A/R	TAPE, DOUBLE SIDED	9007834	14
1	PHIL 5-REW	1210493-51	13
2	PIN CONTACT	1215241	12
2	TIE WRAPS	9007031	11
1	CABLE LABEL	9009532	10
1	LABEL, TMS SIDE UP	3611567	9
23	PW 20-30 AWG	1210993-43	8
23	SOCKET, PWT-68	1210889-5	7
1	HOUSING, 20303 BERG	1210918-15	6
1	SHELL AND INSERT MALE	1210493-31	5
A/R	TUBING, #22 AWG TEF BLK	9107256-00	4
A/R	WIRE, #26 AWG STRD TEF BLK	9107636-00	3
A/R	WIRE, #26 AWG STRD TEF RED	9107636-22	2
A/R	CABLE, 25 CONDUCTOR #26 AWG	9107736	1

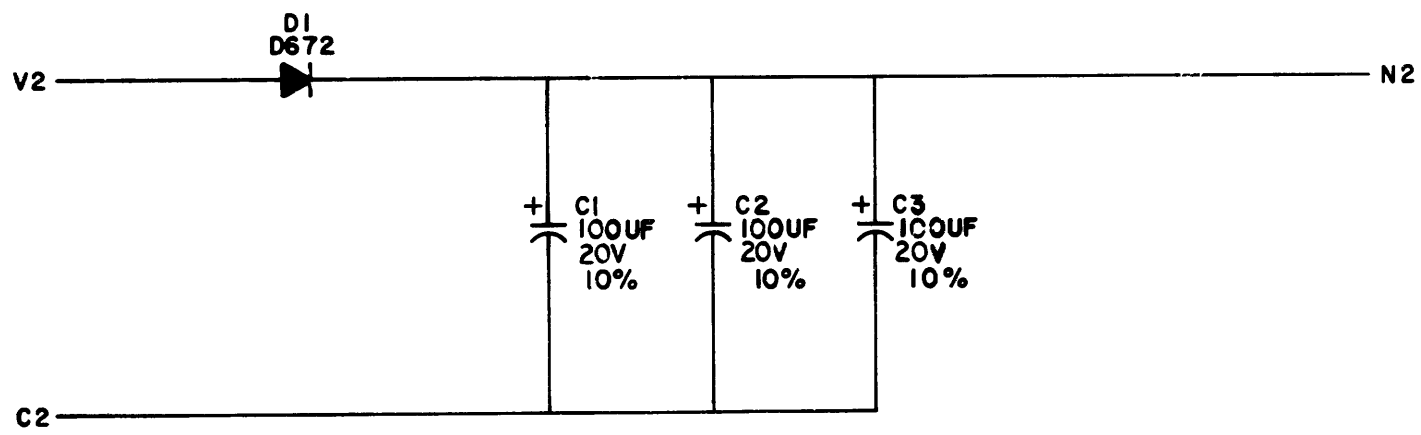
REV.	DATE	BY	CHKD.	DESCRIPTION
1	7-14-78	SMITH		REVISED TO 25 CONDUCTOR
2	10-29-78	SMITH		REVISED TO 25 CONDUCTOR
3	12-14-78	SMITH		REVISED TO 25 CONDUCTOR
4	1-11-79	SMITH		REVISED TO 25 CONDUCTOR
5	2-14-79	SMITH		REVISED TO 25 CONDUCTOR
6	3-14-79	SMITH		REVISED TO 25 CONDUCTOR
7	4-14-79	SMITH		REVISED TO 25 CONDUCTOR
8	5-14-79	SMITH		REVISED TO 25 CONDUCTOR
9	6-15-79	SMITH		REVISED TO 25 CONDUCTOR
10	7-15-79	SMITH		REVISED TO 25 CONDUCTOR
11	8-15-79	SMITH		REVISED TO 25 CONDUCTOR
12	9-15-79	SMITH		REVISED TO 25 CONDUCTOR
13	10-15-79	SMITH		REVISED TO 25 CONDUCTOR
14	11-15-79	SMITH		REVISED TO 25 CONDUCTOR
15	12-15-79	SMITH		REVISED TO 25 CONDUCTOR

FIRST USED ON OPTION/MODEL	DN11	DATE	11/17/78
UNLESS OTHERWISE SPECIFIED DIMENSION IN INCHES TOLERANCES		DATE	11/17/78
DECIMALS	.XXX - .008	ANGLES	±0° 30'
	.XX - .02		
	X - .1		
REMOVE BURRS AND BREAK SHARP CORNERS SURFACE QUALITY			
MATERIAL	++	NEXT HIGHER ASSY.	++
FINISH	++	SCALE	NONE
		SHEET	1 OF 1

EQUIPMENT CORPORATION
CABLE, MODEM BC05C
 DUA BC05C-0-0 H

REV. A NUMBER 1-0-00089 SIZE CODE B CS

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REVISIONS	CHK	CHG NO.	REV
		10000A	
	S. SHAMMAS		

DRN.	S. COOPER	DATE	1/19/71
CHK'D	R. LEE	DATE	2/13/71
ENG.	R. LEE	DATE	3/11/71
PROD.		DATE	

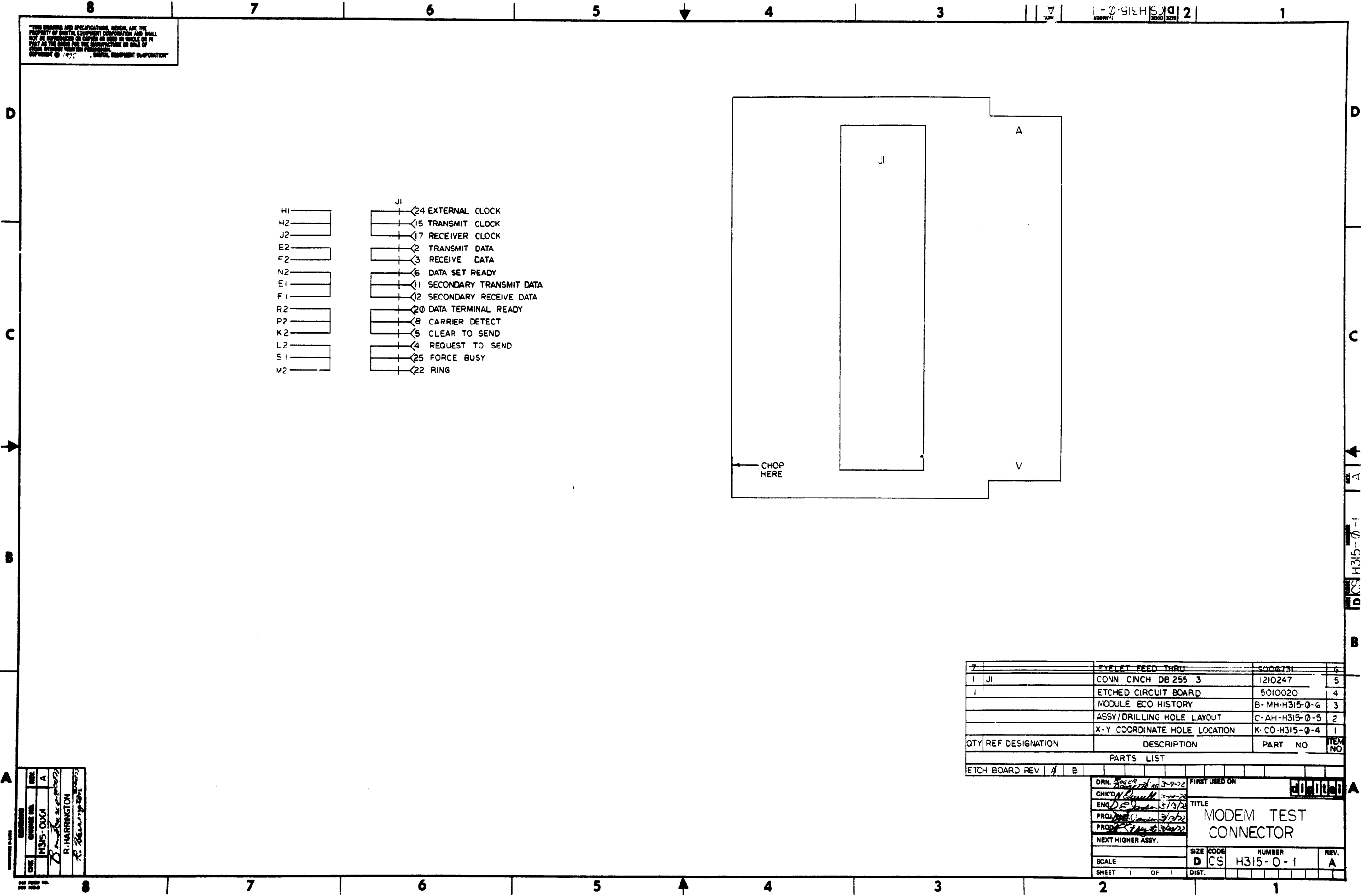
TRANSISTOR & DIODE CONVERSION CHART			
DEC	EIA	DEC	EIA
D672	IN3653		

digital
EQUIPMENT CORPORATION
MAYNARD, MASSACHUSETTS

TITLE FILTER NETWORK G8000			
SIZE	CODE	NUMBER	REV.
B	CS	G8000-0-1	A
PRINTED CIRCUIT REV.			A

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CS H315-0-1 2



QTY	REF DESIGNATION	DESCRIPTION	PART NO	ITEM NO
7		EYELET FEED THRU	S006731	6
1	J1	CONN CINCH DB 255 3	1210247	5
1		ETCHED CIRCUIT BOARD	5010020	4
		MODULE ECO HISTORY	B-MH-H315-0-6	3
		ASSY/DRILLING HOLE LAYOUT	C-AH-H315-0-5	2
		X-Y COORDINATE HOLE LOCATION	K-CO-H315-0-4	1

PARTS LIST			
ETCH BOARD REV	#	B	
DRN. <i>[Signature]</i>	3-9-72	FIRST USED ON	<i>[Stamp]</i>
CHK'D <i>[Signature]</i>	3-10-72	TITLE	MODEM TEST CONNECTOR
ENG <i>[Signature]</i>	3/13/72	SIZE	D
PROJ <i>[Signature]</i>	3/2/72	CODE	CS
PROD <i>[Signature]</i>	3/2/72	NUMBER	H315-0-1
NEXT HIGHER ASSY.		REV.	A
SCALE		DIST.	
SHEET 1	OF 1		

DATE	BY	CHK'D	APP'D
3/2/72	R. HARRINGTON	<i>[Signature]</i>	<i>[Signature]</i>

DIGITAL EQUIPMENT CORPORATION
MAYNARD, MASSACHUSETTS

SOFTWARE LIST

LEGEND
D DOCUMENT
DN DOCUMENT CHANGE NOTICE
PA PAPER TAPE ASCII
PB PAPER TAPE BINARY
PM PAPER TAPE READ-IN-MODE

QUANTITY / VARIATION

MADE BY EMPellegrini
DATE 8/29/72
CHECKED P. Janson
DATE 8-30-72
SECTION
ENG P. Janson
DATE 8/29/72
PROD J. M. Kelly
DATE 8-31-72
ISSUED SECT.

ITEM NO.	DWG NO. / PART NO.	DESCRIPTION	QUANTITY / VARIATION					KIT CHECK	BY DATE		INSTALLATION CHECK	
			DL11-A	DL11-B	DL11-C	DL11-D	DL11-E		BY	DATE	BY	DATE
1	LIBKIT-11-KL11-04	KL11 MAINDEC	1	1	0	0	0					
2	LIBKIT-11-DL11C-A-K	DL11 MAINDEC	0	0	1	1	0					
3	LIBKIT-11-DL11E-A-K	DL11 MAINDEC	0	0	0	0	1					

TITLE DL11 SOFTWARE LIST	ASSY. NO. SHEET 1 OF 1	SIZE CODE A SL	NUMBER DL11-0-4	REV.	ECO NO
DIST.					

DIGITAL EQUIPMENT CORPORATION MAYNARD, MASSACHUSETTS					DATE 6-21-72	
TITLE DL11 INSTALLATION PROCEDURE						
REVISIONS						
REV	DESCRIPTION	CHG NO	ORIG	DATE	APPD BY	DATE
C	CHANGE PER ECO	DL11-4	JANSON	3/73	<i>P. Janson</i>	4-6-73
D	CHANGE PER ECO	DL11-5	CONDON	7/73	<i>P. Condon</i>	8/6/73
E	CHANGE PER ECO	DL11-7	CONDON	8/74	<i>P. Condon</i>	8/21/74
F	CHANGE PER ECO	DL11-8	CONDON	4-75	<i>P. Condon</i>	4/18/75
H	CHANGE PER ECO	DL11-10	HARRINGTON	3-78	<i>P. Harrington</i>	12/22/78

ENG	PAUL E. JANSON	APPD	<i>Paul E. Janson</i>	SIZE	CODE	NUMBER	REV
DEC FORM NO.	DRA 107A			A	SP	DL11-0-2	H
				MK		SHEET 1 OF 11	

ENGINEERING SPECIFICATION		CONTINUATION SHEET	
TITLE DL11 INSTALLATION PROCEDURE			
DL11 INSTALLATION PROCEDURE:			
Installation of the M7800 module or its variation as a DL11-A through DL11-E option consists of the following preparations:			
1. Jumper insertion/deletion for selection of operation mode (A, B, C, D, or E, TO MEET CUSTOMER'S REQUIREMENTS).			
2. Register address assignment.			
3. Vector address assignment.			
4. Priority assignment.			
5. Special NPR jumper insertion/deletion.			
6. Selection of data format (data bits, stop bits, parity).			
7. Selection of crystal for baud rate.			
8. Installation of 68000 in systems where +15v is not available.			
9. Filter capacitor selection for high baud rate current-loop.			
A. OPERATION MODE:			
The following describes the jumpers associated with controlling the mode of operation (A,B,C,D, or E):			
J1. Ties EIA driver to REQUEST-TO-SEND lead (pin 4) of dataset cable, IN for DL11-B,D, and E; does not affect DL11-A and C. Drawing DL-7.			
J2. Ties EIA driver, normally used for the REQUEST-TO-SEND lead, to FORCE BUSY lead (pin 25) for use with Bell 103E. This is a customer option. If not specified, jumper is OUT for all DL11's. Drawing DL-7.			
J3. When inserted, allows REQUEST-TO-SEND lead (pin 4) to be controlled by bit 2 of the receiver status register. OUT for DL11-B and D; IN for DL11-E; does not affect DL11-A and C. Drawing DL-4.			
J4. When inserted, forces "DATA LEADS ONLY" mode of EIA operation. Turns DATA TERMINAL READY (pin 20) and REQUEST-TO-SEND (pin 4) on. IN for DL11-B and D; OUT for DL11-E; does not affect DL11-A and C. Drawing DL-4.			
J5. When inserted, allows the BREAK bit to function. OUT for DL11-A and B; IN for DL11-C,D, and E. Drawing DL-4.			
J6. When inserted, allows DSET INT to cause interrupts. OUT for DL11-A,B,C and D; IN for DL11-E. Drawing DL-4.			
J7. When inserted, allows dataset control bits to be read as part of the receiver status register.			
DEC FORM NO		DL11-0-2	
DRA 107A		MK SHEET 2 OF 11	

ENGINEERING SPECIFICATION		CONTINUATION SHEET	
TITLE DL11 INSTALLATION PROCEDURE			
B. REGISTER ADDRESS ASSIGNMENTS:			
The DL11 can respond to addresses with the following format:			
17	16	15	14
1	1	1	1
13	12	11	10
9	8	7	6
5	4	3	2
1	0	JUMPERS	
Selects 1 of 4 Registers			
Byte Control			
Bits 10 through 3 are controlled by jumpers A10 to A3. A jumper inserted indicates a zero.			
For the DL11-A and B used as the console device, address 777560 is assigned. For additional units, assign 776XX0, where XX=50 for the first additional unit and XX=67 for the 16th unit.			
For the DL11-C,D and E assign address 77XXX0, where XXX=561 for the first line, and XXX=617 for the 31st line. Assign all C's first, then D's, and then E's.			
DEC FORM NO		DL11-0-2	
DRA 107A		MK SHEET 3 OF 11	

ENGINEERING SPECIFICATION		CONTINUATION SHEET	
TITLE DL11 INSTALLATION PROCEDURE			
C. VECTOR ADDRESS ASSIGNMENT:			
Jumpers V8 through V3 control the interrupt vector. A jumper inserted provides a vector bit of one. Vectors can be produced in the form XX# and XX4 where XX ranges from #0 to 77.			
For the DL11-A and B used as a console device the vector address is 060/064. For additional units vectors are floating.			
For the DL11-C,D, and E vector addresses are floating. Assign all C's first, then D's, then E's.			
D. PRIORITY ASSIGNMENT:			
Interrupt priority is established by inserting a "priority plug" in the socket at IC location E19. For DL11-A,B,C,D and E use level 4, for the standard assignment or level 5-7 as specified by the customer or the documentation of an option which uses the DL11.			
SUMMARY OF REGISTER, VECTOR AND PRIORITY ASSIGNMENTS:			
	ADDRESS	VECTOR	PRIORITY
DL11-A,B CONSOLE	777560	60/64	BR4
	777562		
	777564		
	777566		
DL11-A,B ADDITIONAL UNITS	776XX#	FLOATING	BR4
	776XX2		
	776XX4		
	776XX6		
Where XX= 50 for line #1 and XX= 67 for line #16			
	ADDRESS	VECTOR	PRIORITY
DL11-C,D,E	77XXX#	Floating	4
	77XXX2		
	77XXX4		
	77XXX6		
Where XXX= 561 for line #1 and XXX= 617 for line #31			
DEC FORM NO		DL11-0-2	
DRA 107A		MK SHEET 4 OF 11	

ENGINEERING SPECIFICATION	CONTINUATION SHEET																																																							
TITLE DL11 INSTALLATION PROCEDURE																																																								
<p>E. SPECIAL NPJ JUMPER: Jumper N1, shown on drawing DL-6, controls the response of the interrupt circuit to an NPR request. The jumper should normally be IN, except for 11/20 and 11/15 systems without the KH11 option.</p> <p>F. SELECTION OF DATA FORMAT:</p> <p>1. Data Bits Split lug pairs NB2 and NB4 control the number of data bits in the serial character as follows:</p> <table border="1" style="margin-left: 20px;"> <thead> <tr> <th>NB2</th> <th>NB4</th> <th># OF DATA BITS</th> </tr> </thead> <tbody> <tr><td>OUT</td><td>OUT</td><td>8</td></tr> <tr><td>OUT</td><td>IN</td><td>7</td></tr> <tr><td>IN</td><td>OUT</td><td>6</td></tr> <tr><td>IN</td><td>IN</td><td>5</td></tr> </tbody> </table> <p>2. Parity Parity is controlled by split lug pairs NP and EPS as follows:</p> <table border="1" style="margin-left: 20px;"> <thead> <tr> <th>NP</th> <th>EPS</th> <th>PARITY</th> </tr> </thead> <tbody> <tr><td>OUT</td><td>OUT</td><td>OFF</td></tr> <tr><td>OUT</td><td>IN</td><td>OFF</td></tr> <tr><td>IN</td><td>OUT</td><td>EVEN</td></tr> <tr><td>IN</td><td>IN</td><td>ODD</td></tr> </tbody> </table> <p>3. Stop Bits Split lug pair 2SB and jumpers J9, J10 and J11 control the number of stop bits in the serial character as follows:</p> <table border="1" style="margin-left: 20px;"> <thead> <tr> <th>2SB</th> <th>J9</th> <th>J10</th> <th>J11</th> <th># OF STOP BITS</th> </tr> </thead> <tbody> <tr><td>OUT</td><td>OUT</td><td>IN</td><td>OUT</td><td>1</td></tr> <tr><td>IN</td><td>OUT</td><td>IN</td><td>OUT</td><td>1</td></tr> <tr><td>IN</td><td>OUT</td><td>OUT</td><td>IN</td><td>1.5 for TI, GI, and SMC UARTS</td></tr> <tr><td>IN</td><td>IN</td><td>OUT</td><td>OUT</td><td>1.5 for MD UARTS</td></tr> </tbody> </table> <p>G. CRYSTAL SELECTION: The clocking scheme of the DL11 consists of a single crystal oscillator feeding a divider network, with two 10-position switches tapping various points to feed into the UART's</p>		NB2	NB4	# OF DATA BITS	OUT	OUT	8	OUT	IN	7	IN	OUT	6	IN	IN	5	NP	EPS	PARITY	OUT	OUT	OFF	OUT	IN	OFF	IN	OUT	EVEN	IN	IN	ODD	2SB	J9	J10	J11	# OF STOP BITS	OUT	OUT	IN	OUT	1	IN	OUT	IN	OUT	1	IN	OUT	OUT	IN	1.5 for TI, GI, and SMC UARTS	IN	IN	OUT	OUT	1.5 for MD UARTS
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SIZE CODE A SP	NUMBER DL11-0-2																																																							
REV H	SHEET 5 OF 11																																																							

DEC FORM NO DEC 16-(1981)-1022-N370
DRA 108

ENGINEERING SPECIFICATION	CONTINUATION SHEET																																																		
TITLE DL11 INSTALLATION PROCEDURE																																																			
<p>6. Con't transmitter and receiver sections. Thus, for a given crystal frequency, 8 baud rates are independently selectable for transmit and receive. The two addition switch positions select external clocks.</p> <table border="1" style="margin-left: 20px;"> <thead> <tr> <th>SPEED GROUP</th> <th>1</th> <th>2</th> <th>3</th> <th>4</th> </tr> </thead> <tbody> <tr> <td>POSITION</td> <td>844.8K</td> <td>1.03296M</td> <td>1.152M</td> <td>4.608M</td> </tr> <tr> <td>1*</td> <td>23040</td> <td>36.7</td> <td>48.8</td> <td>50</td> </tr> <tr> <td>2</td> <td>15360</td> <td>55</td> <td>87.3</td> <td>75</td> </tr> <tr> <td>3</td> <td>7680</td> <td>110</td> <td>134.5</td> <td>150</td> </tr> <tr> <td>4</td> <td>3840</td> <td>220</td> <td>269</td> <td>300</td> </tr> <tr> <td>5</td> <td>1920</td> <td>440</td> <td>538</td> <td>600</td> </tr> <tr> <td>6</td> <td>960</td> <td>880</td> <td>1076</td> <td>1200</td> </tr> <tr> <td>7</td> <td>640</td> <td>1320</td> <td>1614</td> <td>1800</td> </tr> <tr> <td>8</td> <td>480</td> <td>1760</td> <td>2152</td> <td>2400</td> </tr> </tbody> </table> <p>*Most counter-clock wise position. To determine a crystal frequency for a non-standard baud rate, pick the position of the closest baud rate in the 1.152MHZ column, and then multiply the non-standard baud rate by the factor for that position. For example, if the customer specifies 1050 baud, this is closest to 1200 baud, position 6. 1050 X 960 = 10080000 = 1.008MHz. The crystal frequency should not fall outside the range of the standard DL11 crystals. Although the above table included only the customer or by other documentation of an option which uses the DL11. DEC part number for the standard crystals are as follows: 844.8 KHZ 18-10245-1* 1.03296 MHZ 18-05501-6 1.152 MHZ 18-05501-5 4.608 MHZ 18-05501-7 *Use A or C cut crystals only. Do not use crystals marked NE-6D. When ordering a special crystal, refer to purchase specification 18-05501 for crystal specification. Inure that transparent vinyl tape (9008269) is applied to the top surfaces of the crystal and mounting brackets to insulate from adjacent modules.</p>		SPEED GROUP	1	2	3	4	POSITION	844.8K	1.03296M	1.152M	4.608M	1*	23040	36.7	48.8	50	2	15360	55	87.3	75	3	7680	110	134.5	150	4	3840	220	269	300	5	1920	440	538	600	6	960	880	1076	1200	7	640	1320	1614	1800	8	480	1760	2152	2400
SPEED GROUP	1	2	3	4																																															
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REV H	SHEET 6 OF 11																																																		

DEC FORM NO DEC 16-(1981)-1022-N370
DRA 108

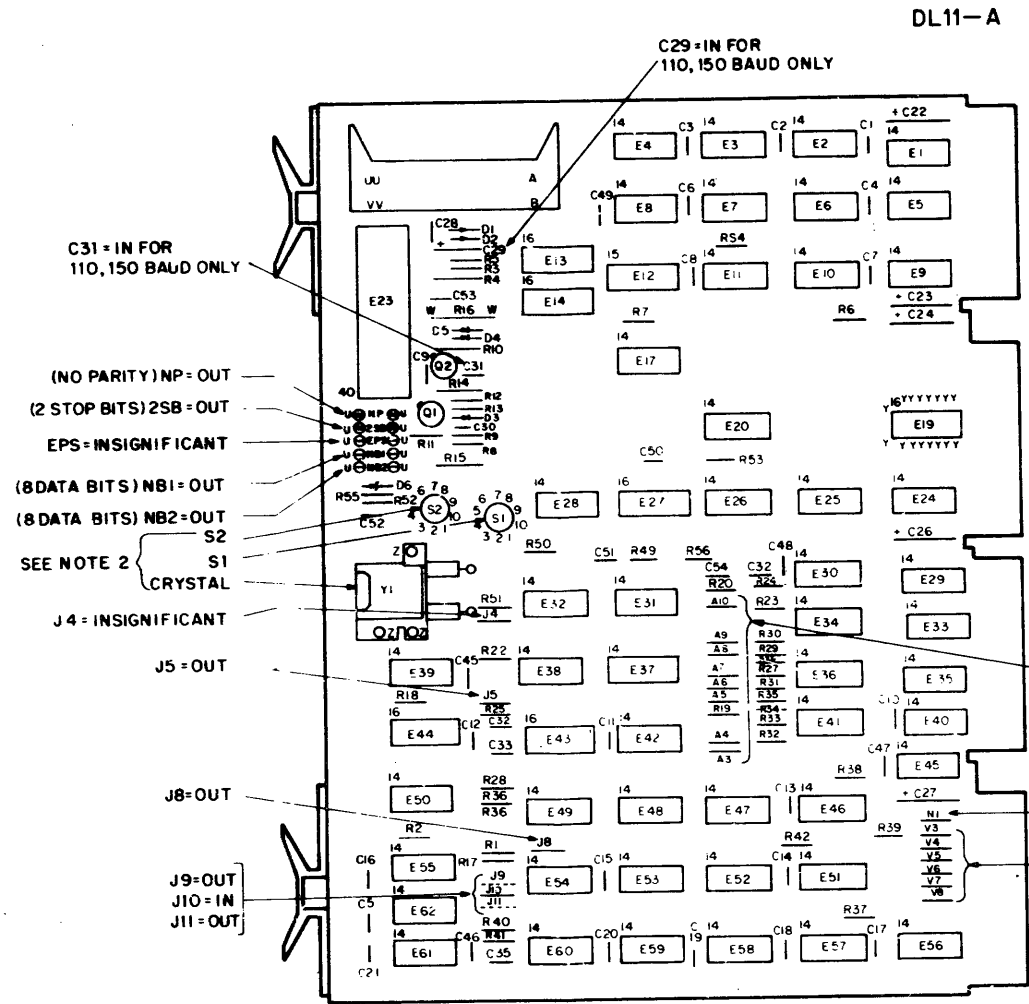
ENGINEERING SPECIFICATION	CONTINUATION SHEET
TITLE DL11 INSTALLATION PROCEDURE	
<p>H. 68000 INSTALLATION: For DL11-B, D, and E a positive voltage is required between 9 and 15 volts to operate the EIA drivers. For PDP-11/20 and PDP-11/15 systems with the M780 power supply, a 68000 module must be installed to provide this voltage. Using a filter network, this module converts the full-wave rectified "+8V" signal to a positive DC voltage.</p> <ol style="list-style-type: none"> Install 68000 into slot AP2 of DD11-A. Wire AP2V2 to AP2VZ. Wire AP2M2 to CAXU1 where XX is the slot location of the M7800. <p>Refer to diagram 1.</p> <p>I. FILTER CAPACITOR SELECTION: For DL11-A's and DL11-C's, which operate with 20ma current loops, capacitors are used to filter the receive line and slow the switching time of the transmit line. To avoid excessive distortion above 150 baud, the capacitance in each of these two circuits must be reduced. This is accomplished by clipping C29 (.47 mfd) and C31 (1000 pf), both shown on drawing DL-3. DL11-B,D,E in Systems with +15V available using DD11-A There is a special situation of using a DD11-A to mount a DL11-B, D, or E in systems with +15V available. These systems have +15V available and it appears at pin AP3V2 of the DD11-A when using power harness such as 7009177, 7008855, or 7008909. In this situation, no 68000 is necessary, and +15V can be wired directly from AP3V2 to CAXU1, where XX is the slot number of the DL11. NOTES: this does not apply to DL11-A or C or DD11-B. K. When using the DL11-F,D,E in an 11/05 processor, pin CAXU1 has +15V available on it so no 68000 or no jumpers are required.</p>	
SIZE CODE A SP	NUMBER DL11-0-2
REV H	SHEET 7 OF 11

DEC FORM NO DEC 16-(1981)-1022-N370
DRA 108

ENGINEERING SPECIFICATION	CONTINUATION SHEET
TITLE DL11 INSTALLATION PROCEDURE	
<p style="text-align: right;">DIAGRAM 1. 68000 INSTALLATION</p>	
SIZE CODE A SP	NUMBER DL11-0-2
REV H	SHEET 8 OF 11

DEC FORM NO DEC 16-(1981)-1022-N370
DRA 108

TITLE DL11 INSTALLATION PROCEDURE



DL11-A

NOTES:

- For further information on the DL11-A configuration or the installation of DL11-B, DL11-C, DL11-D or DL11-E refer to:
 - DL11 Asynchronous Line Interface Manual
 - A-SP-DL11-0-2 (DL11 installation procedure) in the DL11 Engineering Drawings.

SPEED GROUP	1	2	3	4
CRYSTAL FREQ (HZ)	844.8K	1.03296M	1.152M	4.608M
ST. S2 POS.	BAUD RATE			
1	36.7	44.8	50	200
2	55	67.3	75	300
3	110	134.5	150	600
4	220	269	300	1200
5	440	538	600	2400
6	880	1076	1200	4800
7	1320	1614	1800	7200
8	1760	2152	2400	9600

Position 1 is most counter-clockwise position.

ADDRESS

N1 (IN EXCEPT FOR 11/20 & 11/15 SYSTEMS WITHOUT KH11 OPTION)

VECTOR ADDRESS

11-2454

REV H
 NUMBER DL11-0-2
 SIZE CODE A
 SP

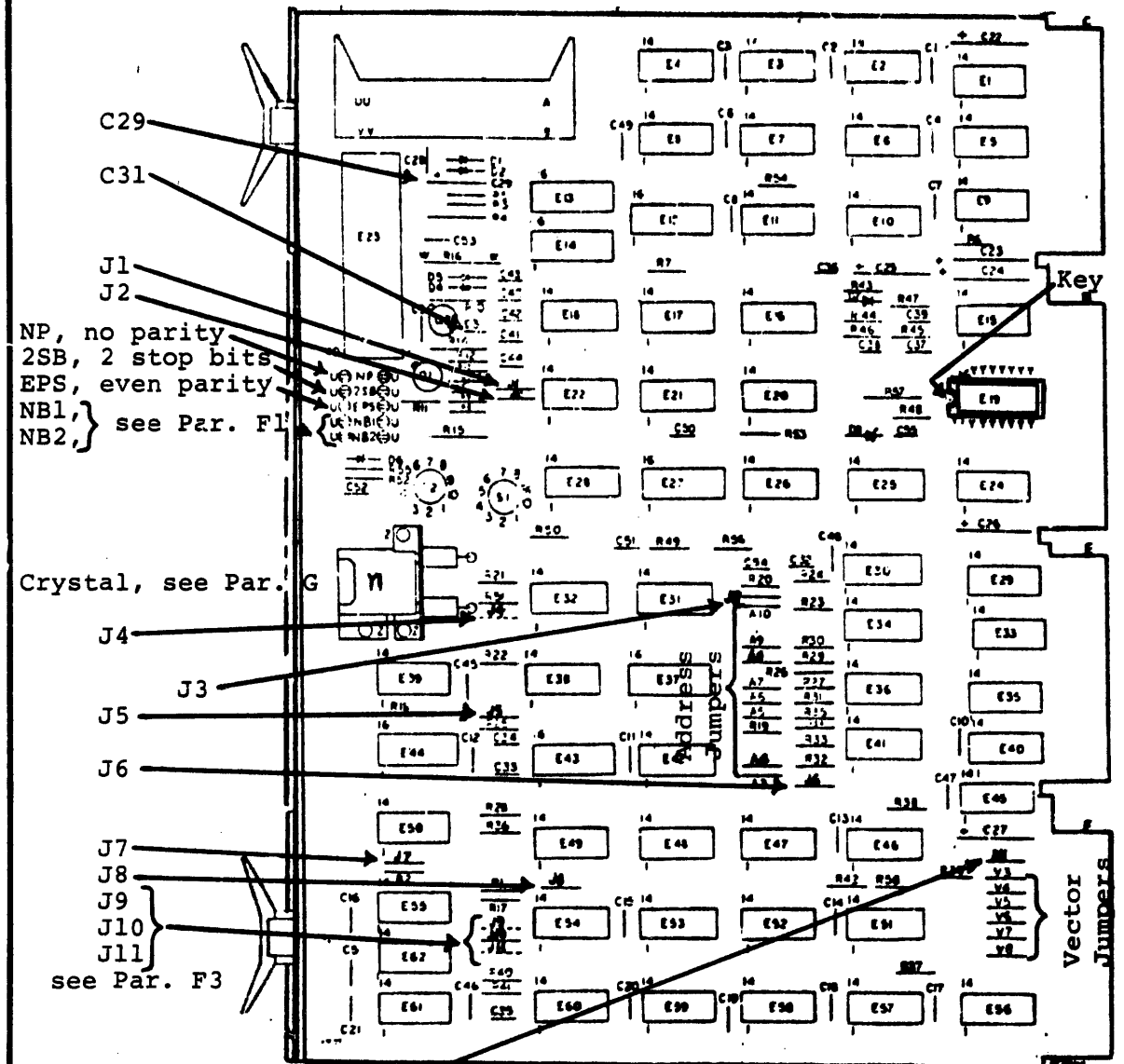
SHEET 9 OF 11

TITLE DL11 INSTALLATION PROCEDURE

DL11-B/D/E
(M7800)

NOTE: For jumper configuration of DL11-B/D/E refer to page 3&5.

C29 and C31 are required for DL11-A and C at 150 BAUD or less, DL11-B,D&E don't care.



NP, no parity
2SB, 2 stop bits
EPS, even parity
NB1, } see Par. F1
NB2, }

Crystal, see Par. G

see Par. F3

N1 (in except for 11/20 and 11/15 without KH11)

SIZE	CODE	NUMBER	REV
A	SP	DL11-0-2	H

TITLE DL11 INSTALLATION PROCEDURE

Figure 1
Identifying Marks for UART (19 10459) Vendors

STANDARD
MICROSYSTEMS



GENERAL
INSTRUMENT



TEXAS
INSTRUMENTS



ADVANCED
MICRO DEVICES



WESTERN DIGITAL



SIZE	CODE	NUMBER	REV
A	SP	DL11-0-2	H