

Authors

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Electronic engineering (M.S.E.E., Stanford University, 1958). Before joining IBM, senior intelligence analyst specializing in analog computer simulation of missile systems and electronic warfare. Joined IBM in 1960, engaged in research in analog, digital, and hybrid computer simulations related to process control systems; in 1965 became project leader for the 1130 Continuous System Modeling Program (CSMP). At present, senior industry development analyst in area of advanced development of digital simulation techniques.

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Electrical engineering (B.S., Illinois Institute of Technology, 1959). Joined IBM in 1961 and participated in the design and evaluation of Teleprocessing systems. Later participated in the design and planning of communication multiplexors for SYSTEM/360 and Binary Synchronous Communications. Currently a staff engineer engaged in small communication multiplexor development.

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Electrical engineering (Ph.D., Yale University, 1957). With General Precision Laboratory in systems and data analysis relating to guidance and control systems before joining IBM in 1959. Has worked on projects in process control, computer-aided instruction, laboratory automation, and applications of digital computers to continuous system simulation problems. Was program manager for development of continuous system modeling programs. Currently is advisory product administrator in area of combined analog-digital (hybrid) computing.

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Electrical engineering (B.E., Yale University, 1955). Joined IBM in 1955. Worked on the design of the STRETCH computer, early SYSTEM/360 development and design of the 7074, 7080, and 7090 emulators for the SYSTEM/360 Model 65. Presently engaged in advanced machine development.