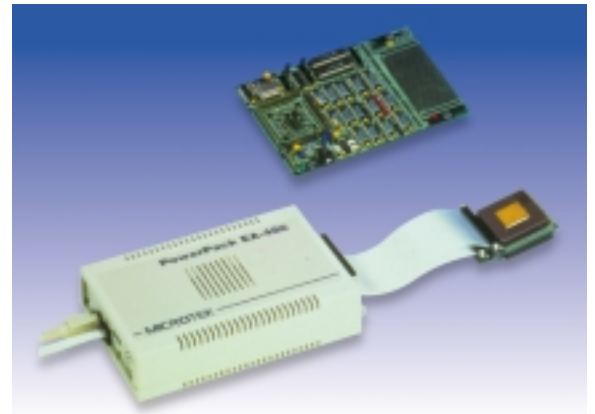


The PowerPack development system consists of a chassis that communicates with the host PC and processor-specific probes that support real-time emulation.

State of the art source level debug support and full emulator control is provided by the Microsoft Windows-based PowerPack SLD interface.

An optional self-test board and supporting software provide a comprehensive confidence test for the entire system. The board also includes a small prototyping area.



Emulation CPU, Supported Targets and Packaging

Emulator	Supported CPU	Maximum Freq.	Emulation Memory	Real-time Trace Buffer	Hardware Breakpoint			Software Performance Analysis	Debugging Software		
					BUS	External	Execution		Assembly	PL/M	C
PowerPack SW 386	386EX, 386CX/SX	25MHz	1M/4MB	128K	-	-	4	✓	✓	-	✓
PowerPack SW Plus 386	386EX, 386CX/SX	33MHz	1M/4MB	128K	-	-	4	✓	✓	-	✓
PowerPack EA 386	386EX, 386CX/SX	33MHz	1M/4MB	256K	8	1	4	✓	✓	-	✓
PowerPack SW plus 486	486DX/DX2/DX4	33/66MHz	1M/4MB	128K	-	-	4	✓	✓	-	✓
PowerPack EA 486	486DX/DX2/DX4	33/66MHz	1M/4MB	256K	8	1	4	✓	✓	-	✓
PowerPack ITP Pentium	Pentium/MMX 75MHz~200MHz	200MHz	-	-	-	-	4	-	✓	-	✓
PowerPack SW Plus Pentium	Pentium/MMX 75MHz~200MHz	200MHz	-	128K	-	-	4	-	✓	-	✓
PowerPack EA Pentium	Pentium/MMX 75MHz~200MHz	200MHz	-	256K	8	1	4	-	✓	-	✓
PowerPack ITP Pentium II/III	Pentium II/MMX, Pentium III, Celeron	500MHz	-	-	-	-	4	-	✓	-	✓
PowerPack SW plus Pentium II/III	Pentium II/MMX, Pentium III, Celeron	500MHz	-	256K	-	-	4	-	✓	-	✓
PowerPack EA Pentium II/III	Pentium II/MMX, Pentium III, Celeron	500MHz	-	256K	8	1	4	-	✓	-	✓
PowerPack 68360	68EN360	25MHz/ 33MHz	256K/1M /4M	128K/256K	8	1	-	-	✓	-	✓
PowerPack 683xx	68331, 68332, 68340, 68F333	20MHz	1/4MB	128K	8	1	-	-	✓	-	✓
PowerPack 68HC16Z1	68HC16Z1	16MHz	1/4MB	128K	8	1	-	-	✓	-	✓

System Features

- Zero wait state operation
- Automatic configuration from 2.7V to 5.5V
- Complete non-intrusive design---uses no target address space, runs full speed with zero wait states
- Long, flexible cables and small profile probe head
- **Uses Intel bondout technology**
- Full support for real, virtual-86, protected and system management modes
- Support for Microsoft, Borland, Metaware, Watcom and other popular compilers
- **Timestamp with clock cycle resolution**
- Peripheral window provides view and edit of all internal registers
- Trace automatically aligns data, executed instruction and source codes
- Conversion of chip selects into logical upper addresses for simple setup of overlay and triggering
- Cursor link from Trace window to Source window showing source code aligned with disassembled trace

ITP

The Pentium ITP (In-Target Probe) provides the engineer with full-featured run control. You can view your code through the debugger as it executes. You can have the ITP start program execution, halt, or step through the code one instruction at a time. Along with the normal run control features, the ITP allows you to view an extensive number of registers within the processor. This includes the 16 typical registers used by the processor, plus debug registers, global descriptor registers, interrupt descriptor registers and floating point registers. The ITP also has an exclusive feature that allows it to decode the model-specific registers (MSRs). These registers provide a history of the errors encountered by the processor. You can view this information and determine the errors causing target failure. The ITP also includes the features needed to support high-level language development and processor control.

SW Plus

The SW Plus provides bus level trace and triggering to provide a full-featured emulator for debugging software development and hardware initialization code.

EA

The EA emulator includes all the above features, plus an exclusive clock-level bus capture feature. The EA emulator is the real solution for system integration and device driver development.

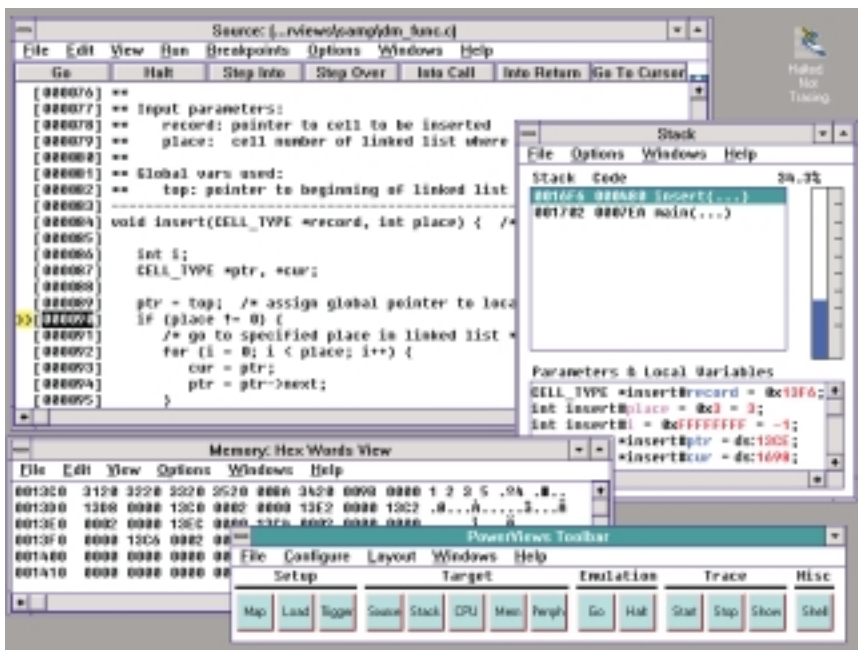
Network Option

The network option allows the emulator to exist on a network as a shared resource.

SWAT

The SWAT(SoftWare Analysis Tool) option lets you perform rigorous testing from the start of a project.

PowerPack SLD Source-Level Interface



- **Source window**, showing emulator halted on line 90 of the function insert()
- **Stack window**, showing local variables in insert() and stack utilization of 34%
- **Memory window**, displaying array contents in memory. Multiple memory windows are supported.
- **Status window (iconized)**, which always displays current emulator status. SLD
- **Toolbar**, which provides fast access to all SLD windows.

Third Party Support

CAD-UL XDB

The CAD-UL XDB interface provides an integrated development environment for users demanding complete C++ support, and a superior set of development tools including optimizing compiler, linker and debugger.

Wind River Tornado

The Wind River Tornado interface provides an integrated debugging package that combines a Microtek emulator with Tornado's VxWorks operating system. The combination provides a strong tool for debugging embedded applications.

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